

**NEXSY Board  
FPGA Pin Assignments  
for On-Board I/O**

function	FPGA pin
LEDs	
LD0	L14
<b>LD1</b>	<b>L13</b>
LD2	M14
<b>LD3</b>	<b>L12</b>
LD4	N14
<b>LD5</b>	<b>M13</b>
LD6	P14
<b>LD7</b>	<b>R16</b>

DISPLAY (4 digit 7-segment display)	
CA	F13
<b>CB</b>	<b>E13</b>
CC	G15
<b>CD</b>	<b>H13</b>
CE	J14
<b>CF</b>	<b>E14</b>
CG	G16
<b>CDP</b>	<b>H14</b>
AN0	G14
<b>AN1</b>	<b>G12</b>
AN2	G13
<b>AN3</b>	<b>F12</b>

BUTTONS	
BTN0	J13
<b>BTN1</b>	<b>K14</b>
BTN2	K13
<b>BTN3</b>	<b>K12</b>

SWITCHES	
SW0	N15
<b>SW1</b>	<b>J16</b>
SW2	K16
<b>SW3</b>	<b>K15</b>
SW4	L15
<b>SW5</b>	<b>M16</b>
SW6	M15
<b>SW7</b>	<b>N16</b>

CLOCKS	
system <small>(25/50/100 MHz)</small>	A8

reverse look-up	
FPGA pin	function
A8	Sys Clock
<b>E13</b>	<b>CB</b>
E14	CF
<b>F12</b>	<b>AN3</b>
F13	CA
<b>G12</b>	<b>AN1</b>
G13	AN2
<b>G14</b>	<b>AN0</b>
G15	CC
<b>G16</b>	<b>CG</b>
H13	CD
<b>H14</b>	<b>CDP</b>
J13	BTN0
<b>J14</b>	<b>CE</b>
J16	SW1
<b>K12</b>	<b>BTN3</b>
K13	BTN2
<b>K14</b>	<b>BTN1</b>
K15	SW3
<b>K16</b>	<b>SW2</b>
L12	LD3
<b>L13</b>	<b>LD1</b>
L14	LD0
<b>L15</b>	<b>SW4</b>
M13	LD5
<b>M14</b>	<b>LD2</b>
M15	SW6
<b>M16</b>	<b>SW5</b>
N14	LD4
<b>N15</b>	<b>SW0</b>
N16	SW7
<b>P14</b>	<b>LD6</b>
R16	LD7