

frame alignment hole over
gap in frame PCB
at both ends

VDDA trace only 100um wide

use this mark only for MCC alignment

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID VERSION 3 ERRATA		
	R. Boyd, University of Oklahoma		
Tolerance: n/a	APFHV3-ER	0	
DATE: NONE 1 October, 2001	REV: 1/1		

SPECIFICATIONS

for

apfhv3

(ATLAS Pixel Flex Hybrid version 3)

----- Alternate specifications must be submitted in writing -----

Order Quantity: 50 pieces

Number of metal layers	2
Substrate material sputtered	25.4 μm [1 mil] polyimide (Upilex preferred) w/sputtered seed metal (e.g., Cr or Ti) and CU starter layer (adhesiveless Cu)
Delivered substrate size	86.6 mm x 19.6 mm \pm 0.10 mm [3.4 in. x 0.7717 in. \pm 0.040 in.]
Metalization	
Base metalization	16 μm [630 μin] minimum
Barrier metal (where required)	2 μm [79 μin] Ni, \pm 0.5 μm [19.7 μin]
Au plating:	No greater than 0.2 μm [7.9 μin].
Top to bottom metal registration tolerance	No via hole breakout of any cover pad
Cover layer (top & bottom)	10 μm [390 μin] to 25.4 μm [1 mil] 1 kV holdoff voltage Coefficient of thermal expansion < 130 ppm/ $^{\circ}\text{C}$ Examples: DuPont PI 2734, Pyralux PC 1010 or PC 2010. Must be hard, scratch resistant and radiation tolerant to 100 Gy
Required cover layer total tolerance (see drawing):	
Bottom	\pm 127 μm [5 mil] maximum
Top	\pm 127 μm [5 mil] maximum

DESIGN FEATURES

Note: Many (> 500) wire bond pads are on a 150 μm [5.9 mil] pitch. The traces from these pads are fanned out to a wider trace and space in a short distance. Most traces and spaces are 100 μm [3.9 mil].

Min. trace width	75 μm [2.9 mil]
Min. trace space	75 μm [2.9 mil]
Min. feature space	

Via cover pad to trace*	100 µm [3.9 mil]
Bond pad space	50 µm [2.0 mil]
Bond pad size	100 µm x 300 µm [3.9 mil x 11.8 mil]
Cover pad size*	200 µm diameter [7.9 mil]
Via hole size*	25.4 µm [1 mil] minimum

* These parameters can be optimized for manufacturer's process

DESIGN FILE NOTES

Gerber files:

Device Gerber photoplotter with RS-274-X aperture generation

Parameter settings:

OffsetX	0.000
OffsetY	0.000

Plotfile Info:

Coordinate Format	2.4
Coordinate Units	Inch
Data Mode	Absolute
Zero Suppression	None
End Of Block	*

Drill file info:

Tolerance Drill +	0.00 %
Tolerance Drill -	0.00 %
Rotate	no
Mirror	no
Optimize	yes
Auto fit	yes
OffsetX	0.000
OffsetY	0.000
Format	2.4 (1/10000 in.)
Zero suppression	None

Drills used:

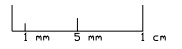
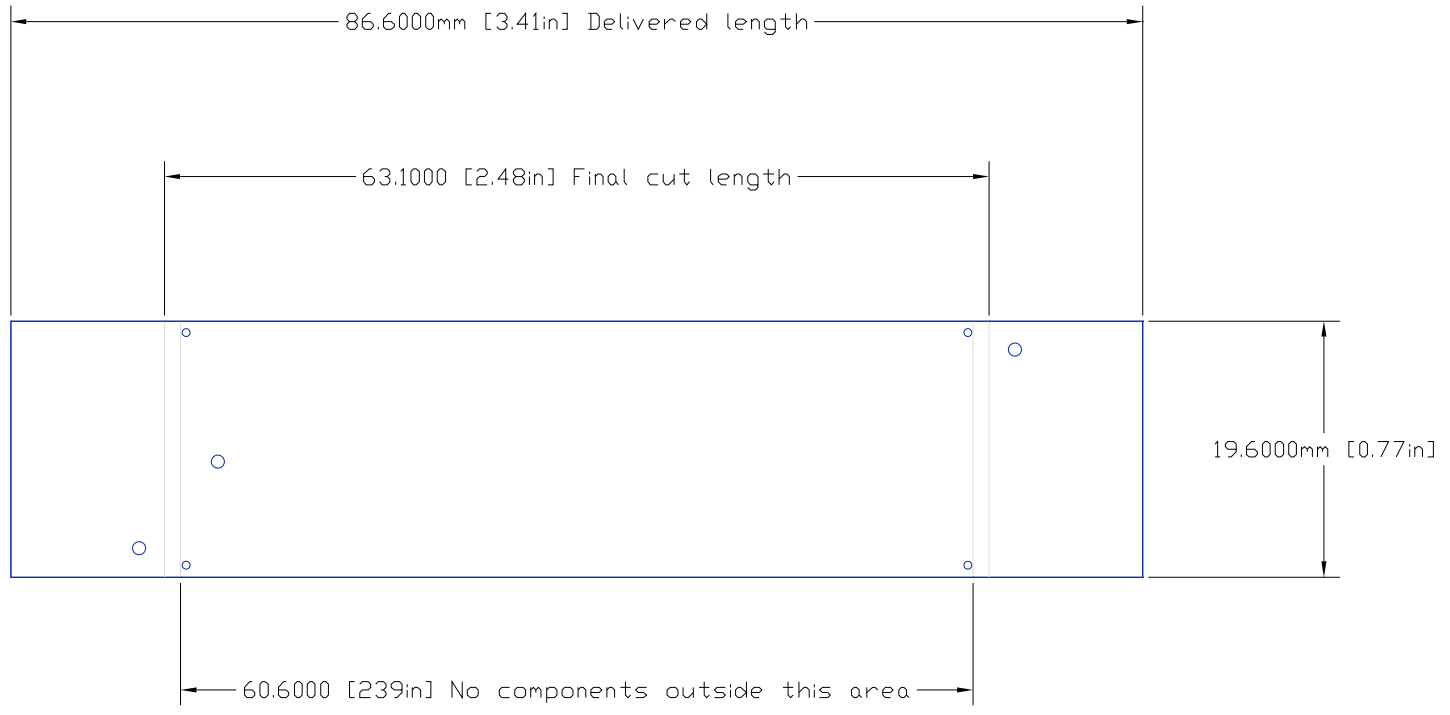
Code	Size	Count	Plated Through
T01	0.050mm**	632	632
T02	0.300mm	4	0
T03	1.000mm	3	1 (see drawing)

** Via drill size can be optimized for fabrication process

FILE DEFINITIONS:

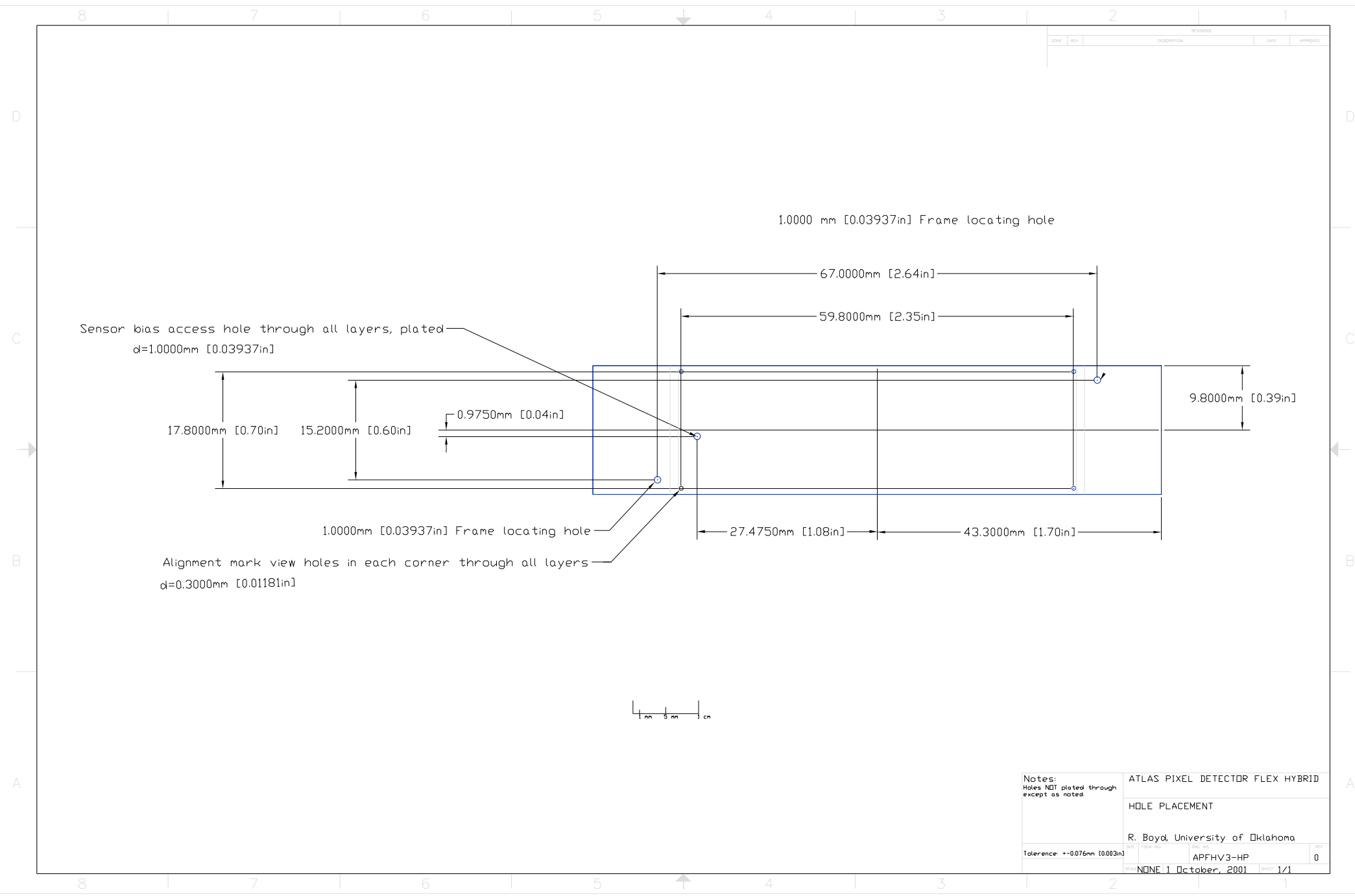
File name	Definition
apfhv3_fab.pdf	This file (specifications, fabrication drawings, schematic, etc.)
apfhv3_gerber.zip	Zip file containing the following:
top.gbr	Top metal layer
bottom.gbr	Bottom metal layer
bottom_mask.gbr	Bottom solder mask, negative
top_mask.gbr	Top solder mask, negative
cut.gbr	Circuit outline (cutting guide)
drill.xcl	Excellon drill file
rack.drl	Drill tool list

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



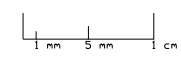
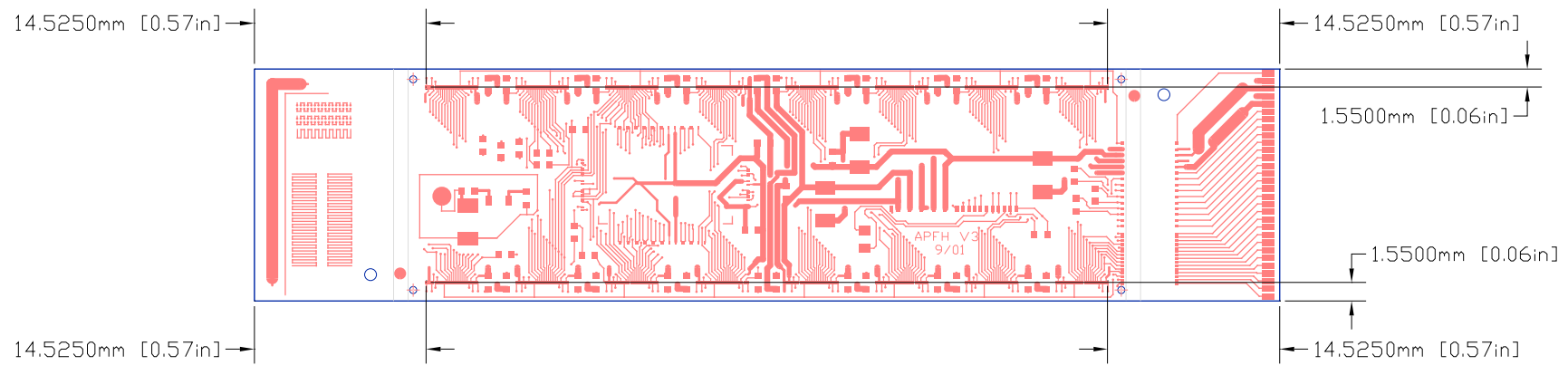
Notes: Flex vendor not responsible for final cut length.	ATLAS PIXEL DETECTOR FLEX HYBRID		
	OUTLINE DIMENSIONS		
Tolerance: $\pm 0.076\text{mm}$ [0.003in]	R. Boyd, University of Oklahoma		REV
	APFHV3-DL		2
REV: NONE 1 October, 2001		DATE	1/1

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



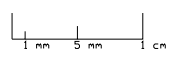
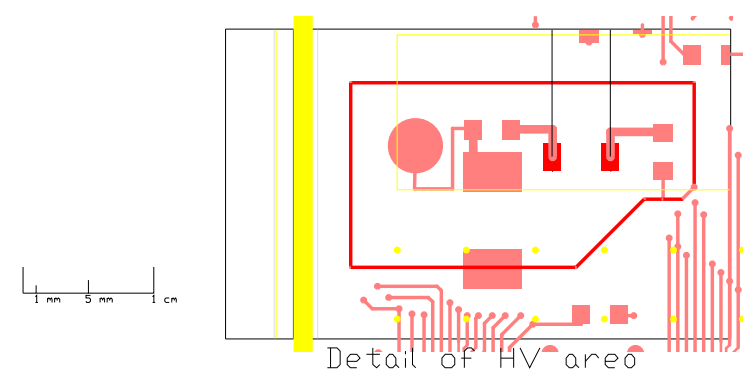
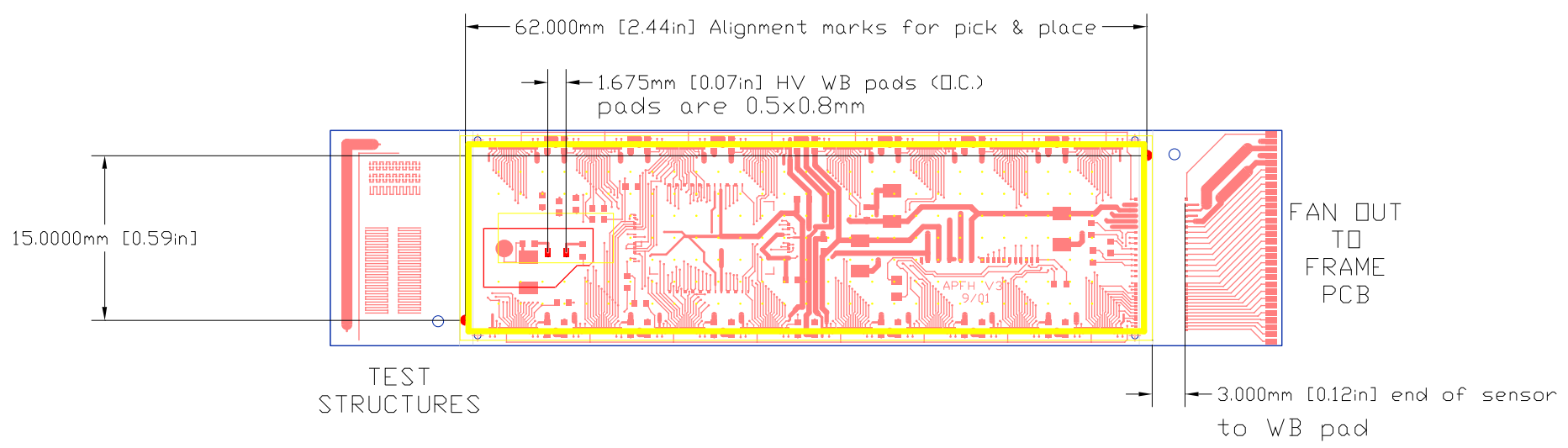
Notes: Holes NOT plated through except as noted.	ATLAS PIXEL DETECTOR FLEX HYBRID			
	HOLE PLACEMENT			
Tolerance: +-0.076mm [0.003in]	R. Boyd, University of Oklahoma			REV
	APFHV3-HP			0
	NONE 1 October, 2001			SHEET 1/1

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



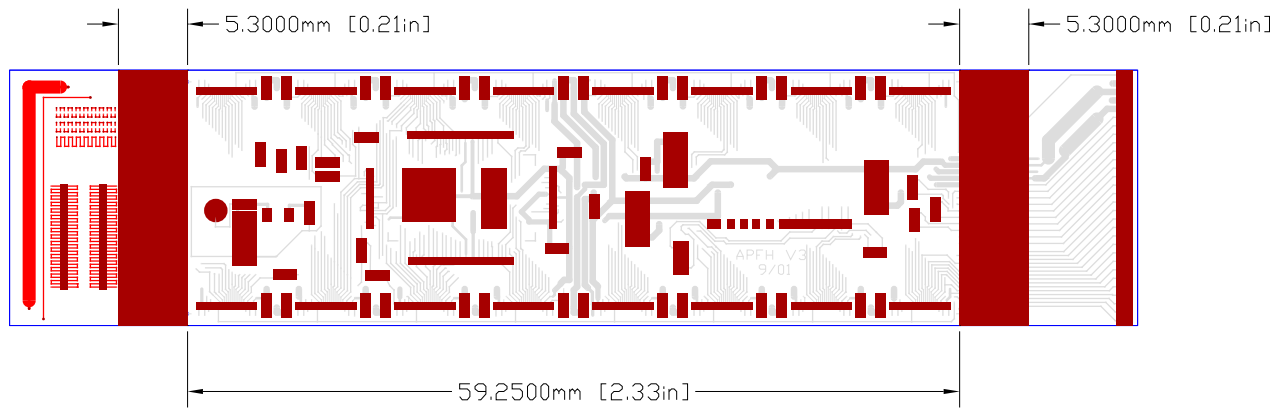
Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID		
	TOP METAL TO OUTLINE DIMENSIONS & TOLERANCES		
Tolerance: $\pm 0.127\text{mm}$ [0.005in]	DESIGNED BY	R. Boyd, University of Oklahoma	
	DATE	APFHV3-MR	2
	REV	NONE 1 October, 2001	1/1

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

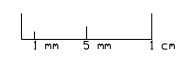


Notes: See following sheets for bond pad dimensions	ATLAS PIXEL DETECTOR FLEX HYBRID		
	TOP METAL FEATURES		
Tolerance: no via breakout	R. Boyd, University of Oklahoma		
		APFHV3-TF	2
	REV: NONE 1	DATE: October, 2001	SHEET: 1/1

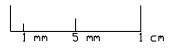
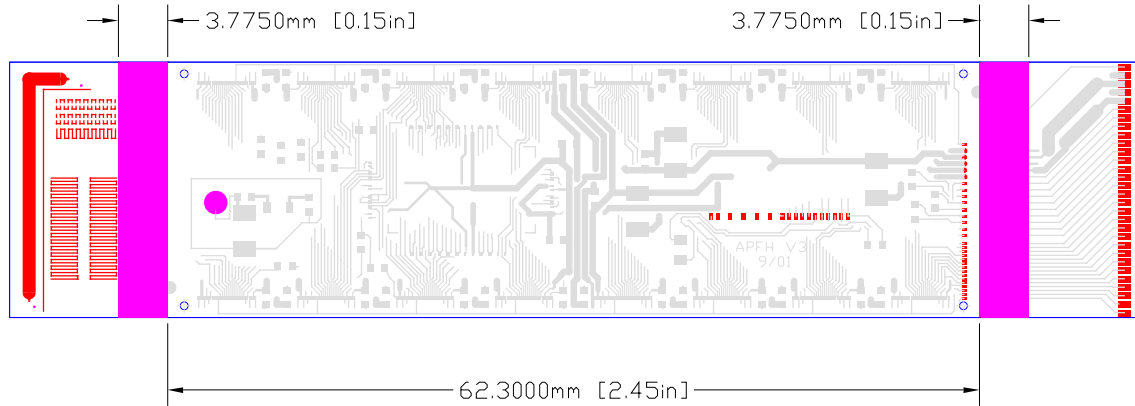
REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID VERSION 3		
	TOP SOLDER MASK		
Placement Tolerance: 0.1016mm [0.004in] wr.t metal	DATE	DESIGNER	REV
Opening Tolerance: 0.0254mm [0.001in]		APFHV3-TM	0
		NONE 1 October, 2001	SHEET 1/1

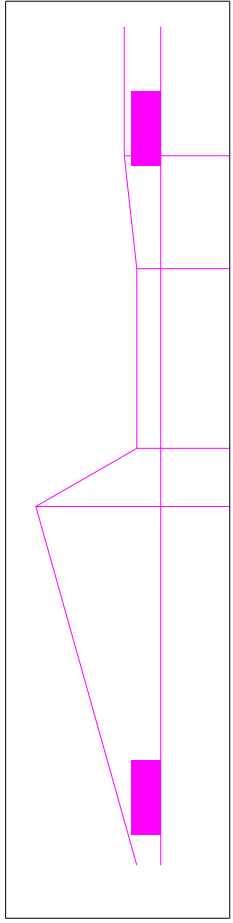
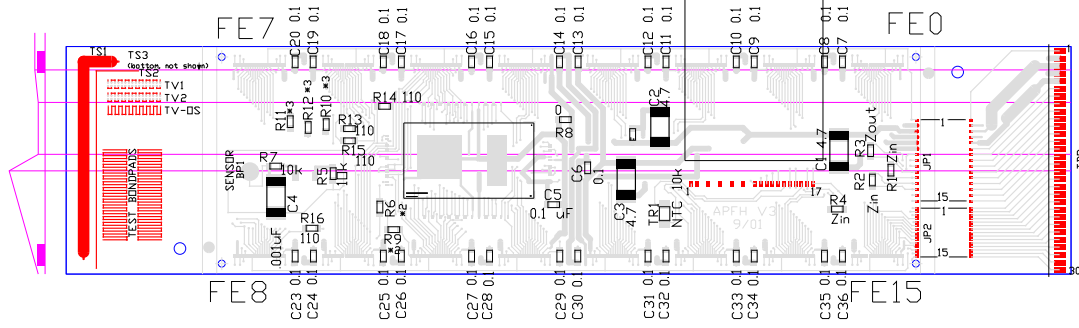


REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID VERSION 3		
	BOTTOM SOLDER MASK		
	R. Boyd, University of Oklahoma		
Placement Tolerance: 0.1016mm [0.004in] w.r.t metal	DATE: 1 October, 2001	DESIGN NO.: APFHV3-BM	REV: 0
Opening Tolerance: 0.0254mm [0.001in]	NONE		SHEET 1/1

REVISION			
ZONE	REV	DESCRIPTION	DATE

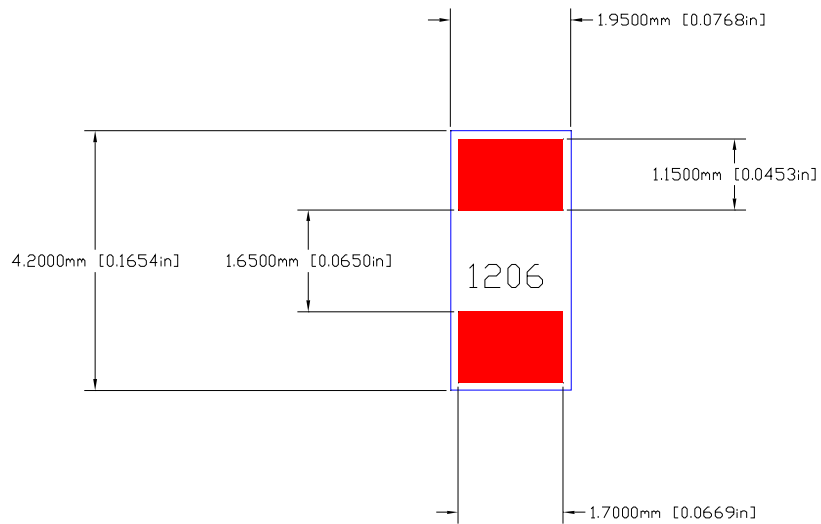


Detail of projection of component heights + tolerances onto envelope (1)

Notes: 1. Component tolerances include: - component height - soldering height - component placement See schematic for #1, #2, #3, component specifications & other assembly notes Tolerance: n/a	ATLAS PIXEL DETECTOR FLEX HYBRID VERSION 3 ASSEMBLY DRAWING
	R. Boyd, University of Oklahoma
	APFHV3-AD NONE 1 October, 2001

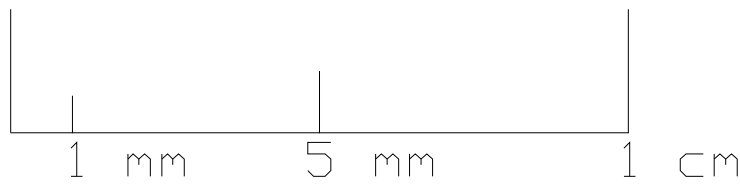
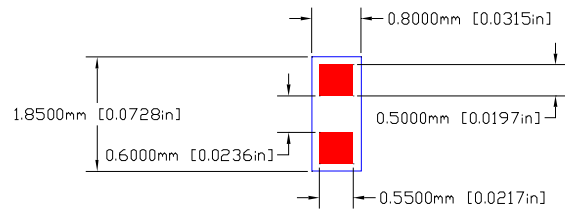
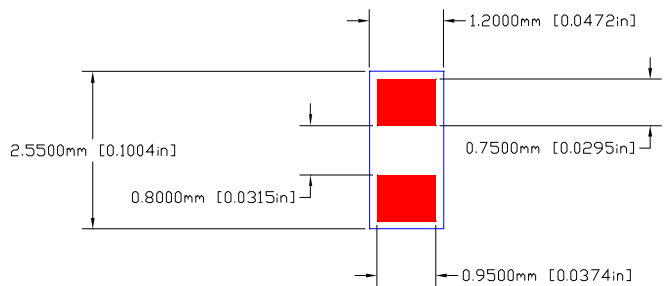
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1/1



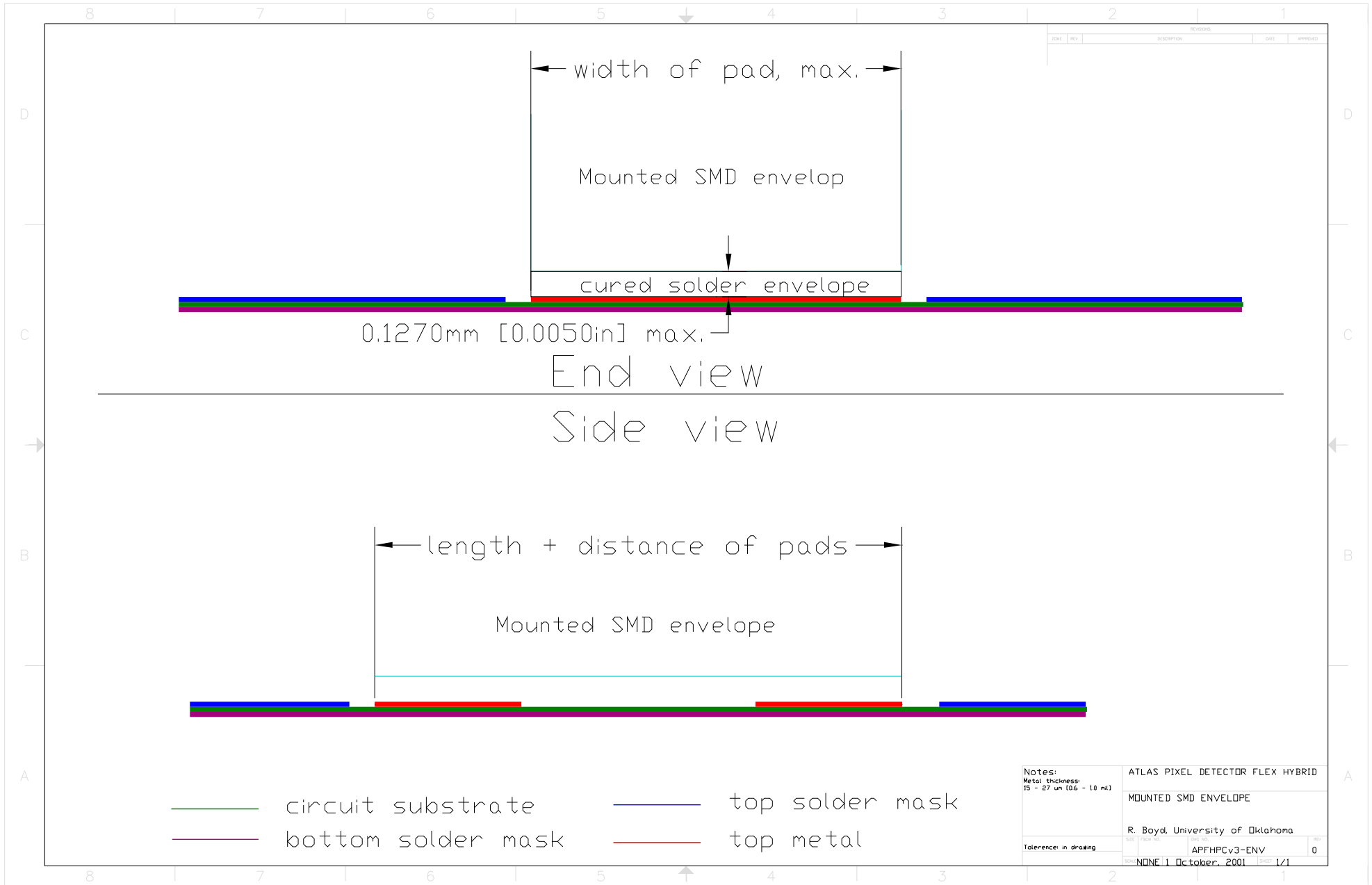
— solder mask opening

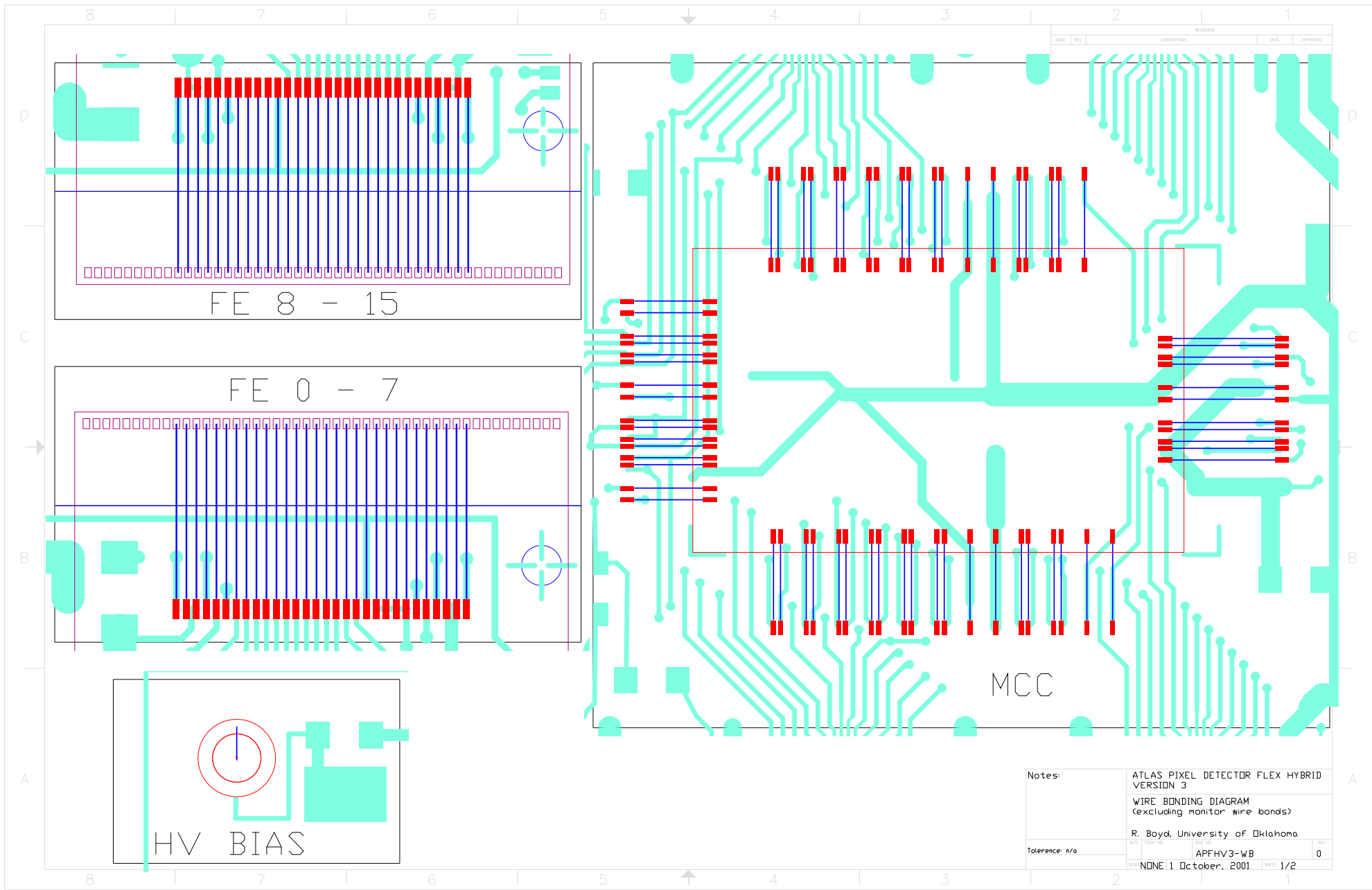
— top metal



REV		DESCRIPTION	DATE	APPROVED

Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID PROBE CARD
Tolerances:	SMD PAD AND SOLDER MASK OPENING SIZES
solder mask w.r.t metal	R. Boyd, University of Oklahoma
-- 0.127mm [5 mil]	APFHPCv3-SMD
metal x-y dim.	0
-- 0.005mm [0.2mil]	DATE: NONE 1 October, 2001
Tolerance: see notes	REV: 1/1





REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

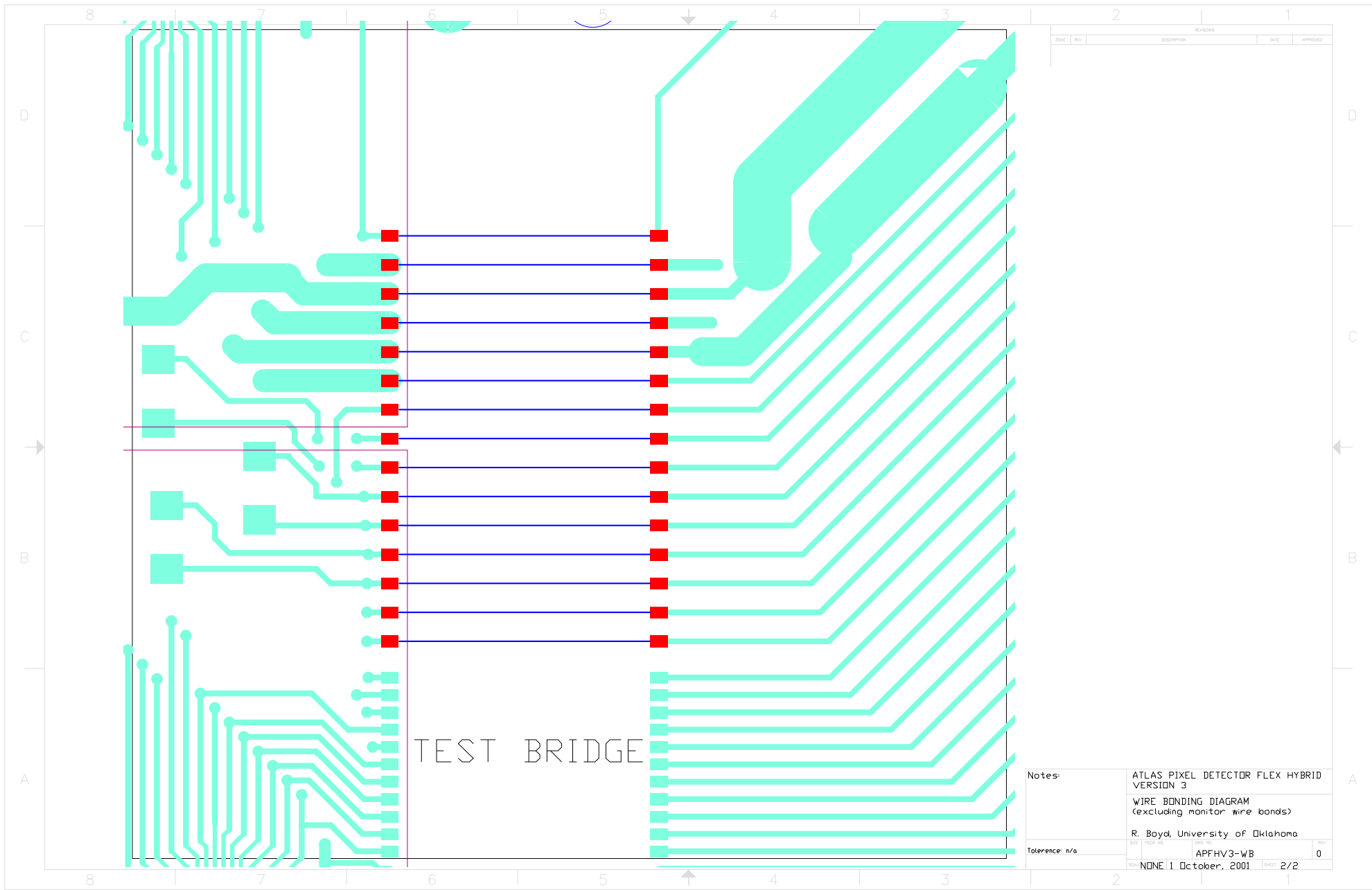
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FE 0 - 7

MCC

HV BIAS

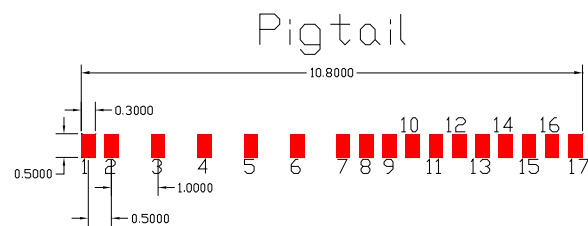
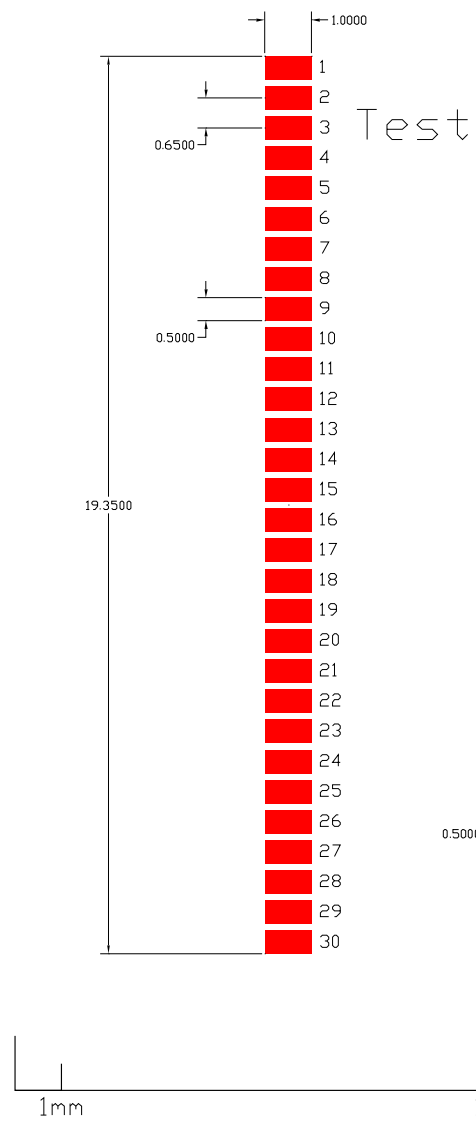
Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID VERSION 3		
	WIRE BONDING DIAGRAM (excluding monitor wire bonds)		
Tolerance: n/a	R. Boyd, University of Oklahoma		0
	APFHV3-WB		
	DATE: NONE 11 October, 2001	SCALE: 1/2	



REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

TEST BRIDGE

Notes:	ATLAS PIXEL DETECTOR FLEX HYBRID VERSION 3 WIRE BONDING DIAGRAM (excluding monitor wire bonds)		
Tolerance: n/a	R. Boyd, University of Oklahoma		0
	DATE	ISSUE NO.	REV
	NONE 1 October, 2001	APFHV3-WB	2/2



ATLAS Pixel Flex Hybrid Module
connector pad def.'s & dim.'s

Drawn by: R. Boyd, University of Oklahoma

Document Number:

REV:

Date: 27 July, 2001

Units: mm

Scale: n/a

V3 and v4.x Pigtail Connector Signals

Pin #	Flex v3 Signal Name	Flex v4.x Signal Name	Pigtail Signal Name
1	Vca1	Vca1	spare1
2	VDD_AMS	VDD_AMS	spare2
3	VDDA	VDDA	VDDA
4	AGnd	AGnd	AGnd
5	VDD	VDD	VDD
6	DGnd	DGnd	DGnd
7	STRIN	DTO2N	STRI/DTO2N
8	STRIP	DTO2P	STRI/DTO2P
9	RSIB	RSIB	RSIB
10	DTON	DTON	DTON
11	DTOP	DTOP	DTOP
12	NTC_RET	NTC_RET	NTC_RET
13	DCIN	DCIN	DCIN
14	DCIP	DCIP	DCIP
15	NTC	NTC	NTC
16	XCKN	XCKN	XCKN
17	XCKP	XCKP	XCKP

ATLAS PIXEL FLEX HYBRID MODULE		
PIGTAIL CONNECTOR		

University of Oklahoma		Scale: n/a
Dwg.#	Rev. A	Units: n/a
Drawn By:	R. Boyd	Date: 27 February, 2002

V3 and v4.x Test Connector Signals

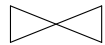
Pad #	Signal Name		Pad #	Signal Name
	3.x	4.x		
1	Vca1		16	CCK
2	VDD_AMS	spare 2	17	DA
3	DGnd		18	LD
4	VDD		19	SYNCP
5	AGnd		20	SYNCP
6	VDDA		21	XCKN
7	RSIB		22	XCKP
8	DTOP		23	LV1N
9	DTON		24	LV1P
10	STRIP	DTO2P	25	STRN
11	STRIN	DTO2N	26	STRP
12	DCIN		27	VDDA
13	DCIP		28	VDD
14	CKN		29	DGnd
15	CKP		30	AGnd

ATLAS PIXEL FLEX HYBRID MODULE			
TEST CONNECTOR			
University of Oklahoma		Scale: n/a	
Dwg.#	Rev. A	Units: n/a	
Drawn By:	R. Boyd	Date:	26 February, 2002

NOTES:

- *1 All resistors in Ohms, 0402, 1/16 W
- *2 Use of R6 and R9 is mutually exclusive! One or the other can be zero Ohms, but not both.
- *3 r_clamp - 10k?
- *4 All capacitors 10 WDC unless otherwise noted
- *5 All capacitors 0402 except: C1 - C4 are 1206
- *6 NTC is 0603

*7

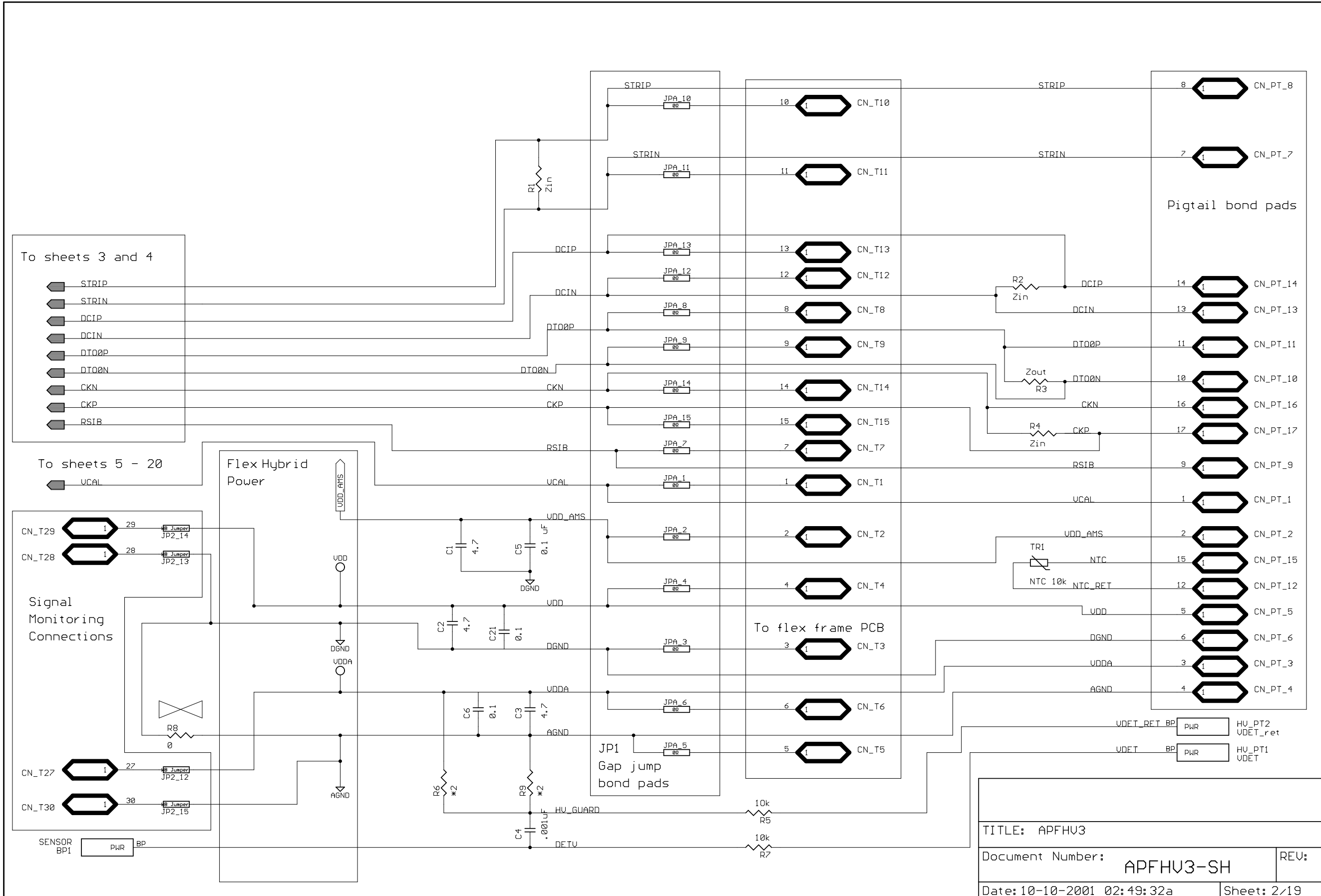


denotes solder pads shorted on layout.

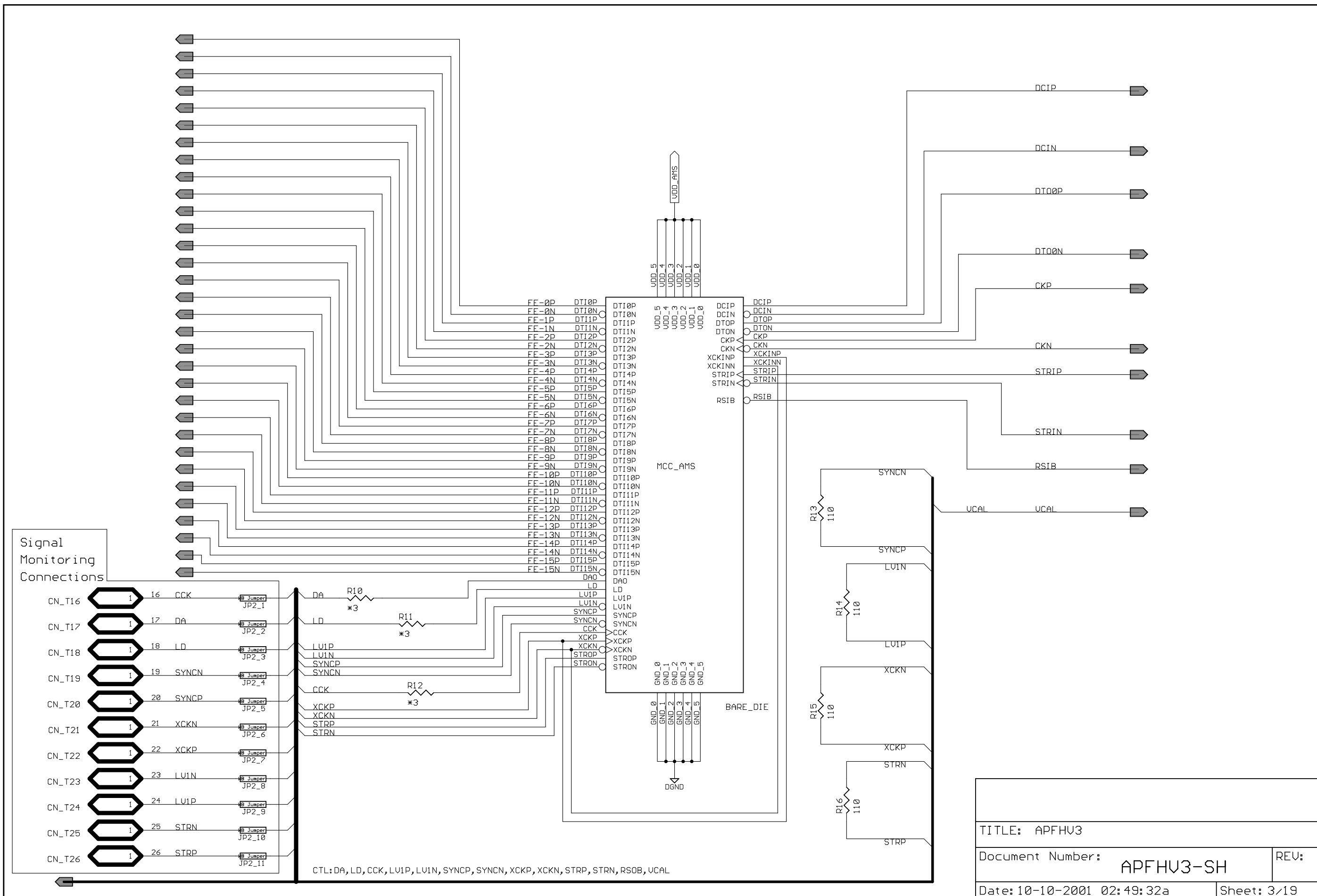
Missing parts designators:

C22

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 Sheet: 2/19

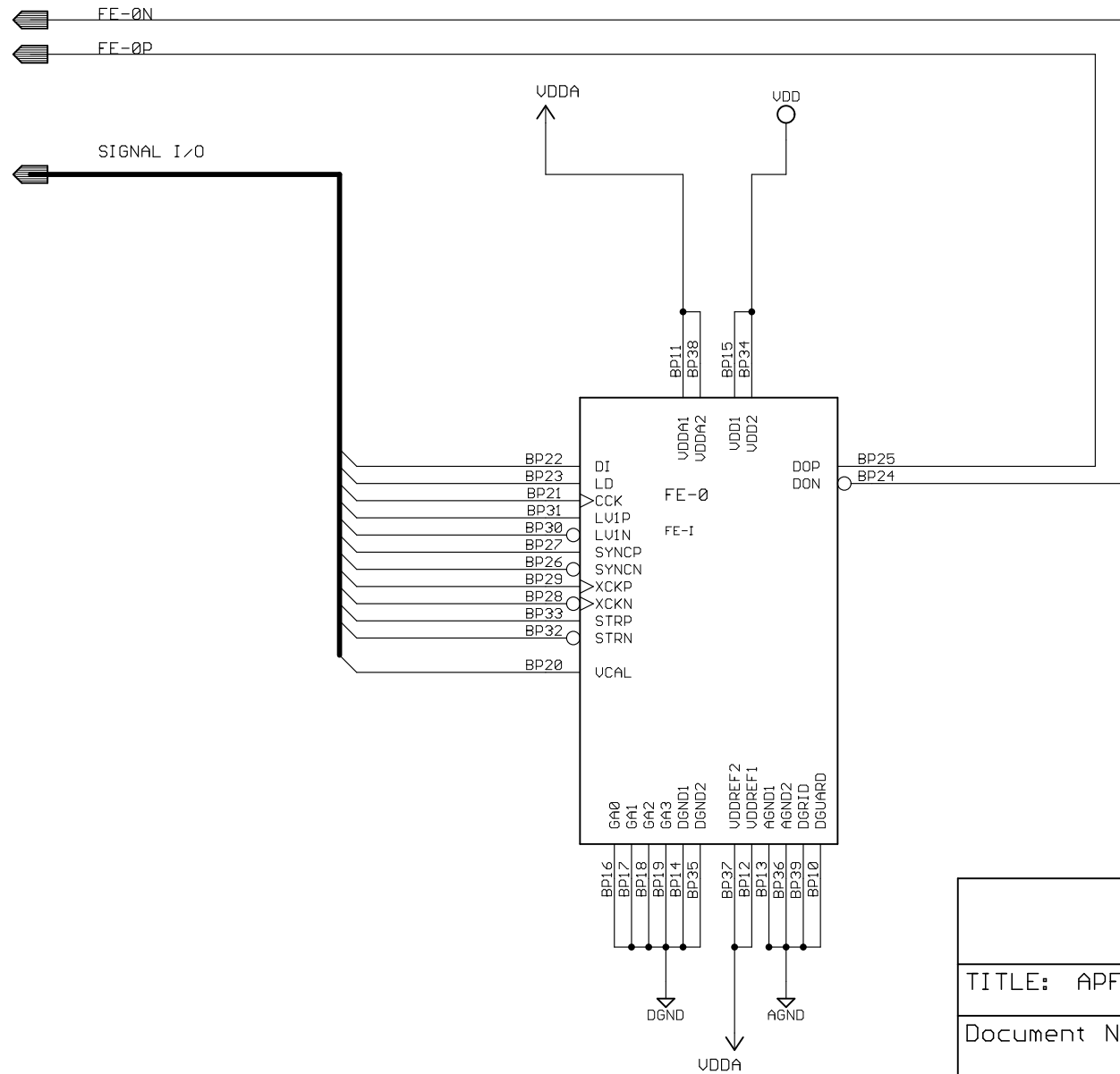


Signal Monitoring Connections

CN_T16	16	CCK	JP2_1
CN_T17	17	DA	JP2_2
CN_T18	18	LD	JP2_3
CN_T19	19	SYNCP	JP2_4
CN_T20	20	SYNCP	JP2_5
CN_T21	21	XCKN	JP2_6
CN_T22	22	XCKP	JP2_7
CN_T23	23	LUIP	JP2_8
CN_T24	24	LUIP	JP2_9
CN_T25	25	STRN	JP2_10
CN_T26	26	STRP	JP2_11

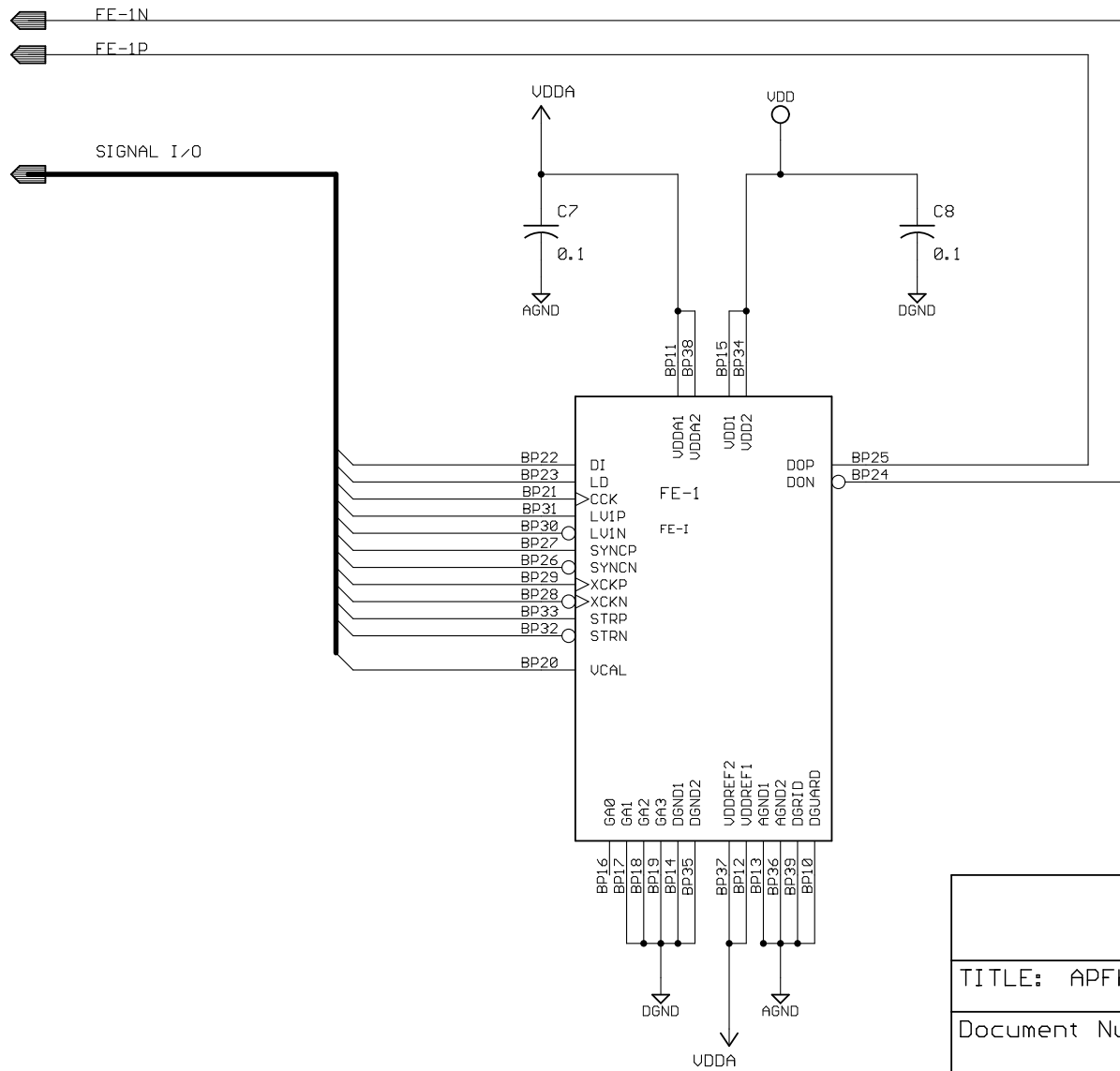
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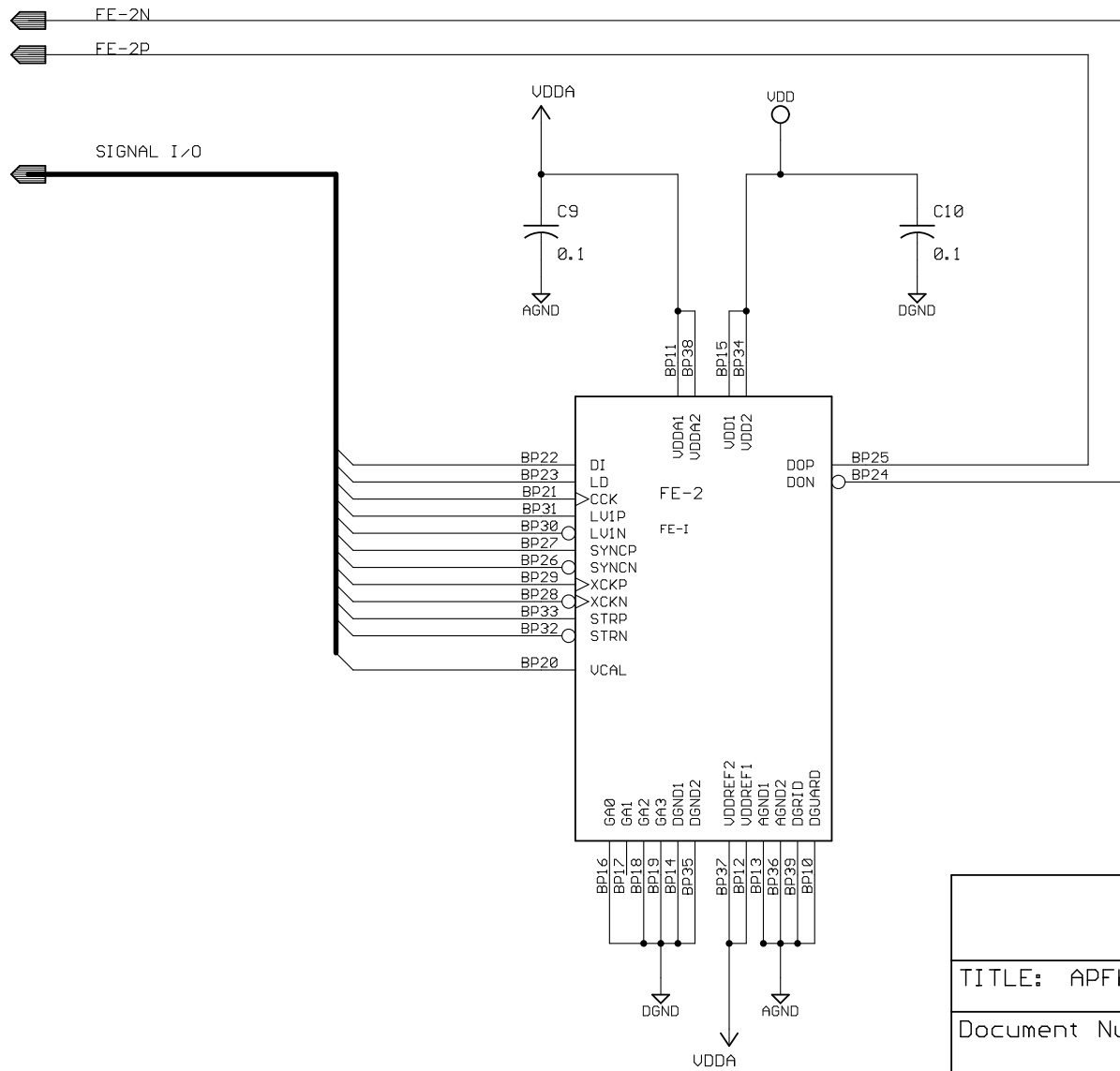
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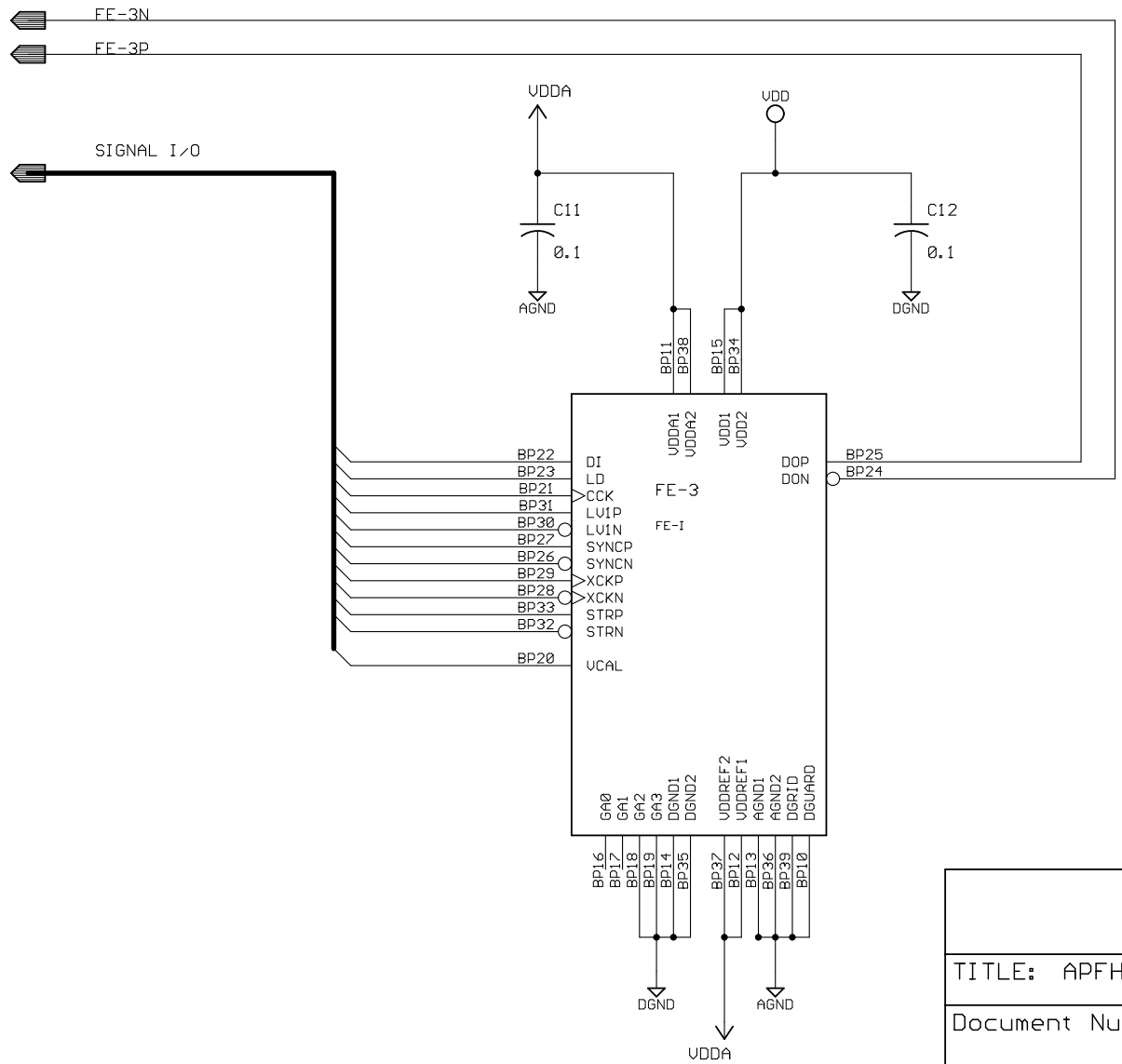
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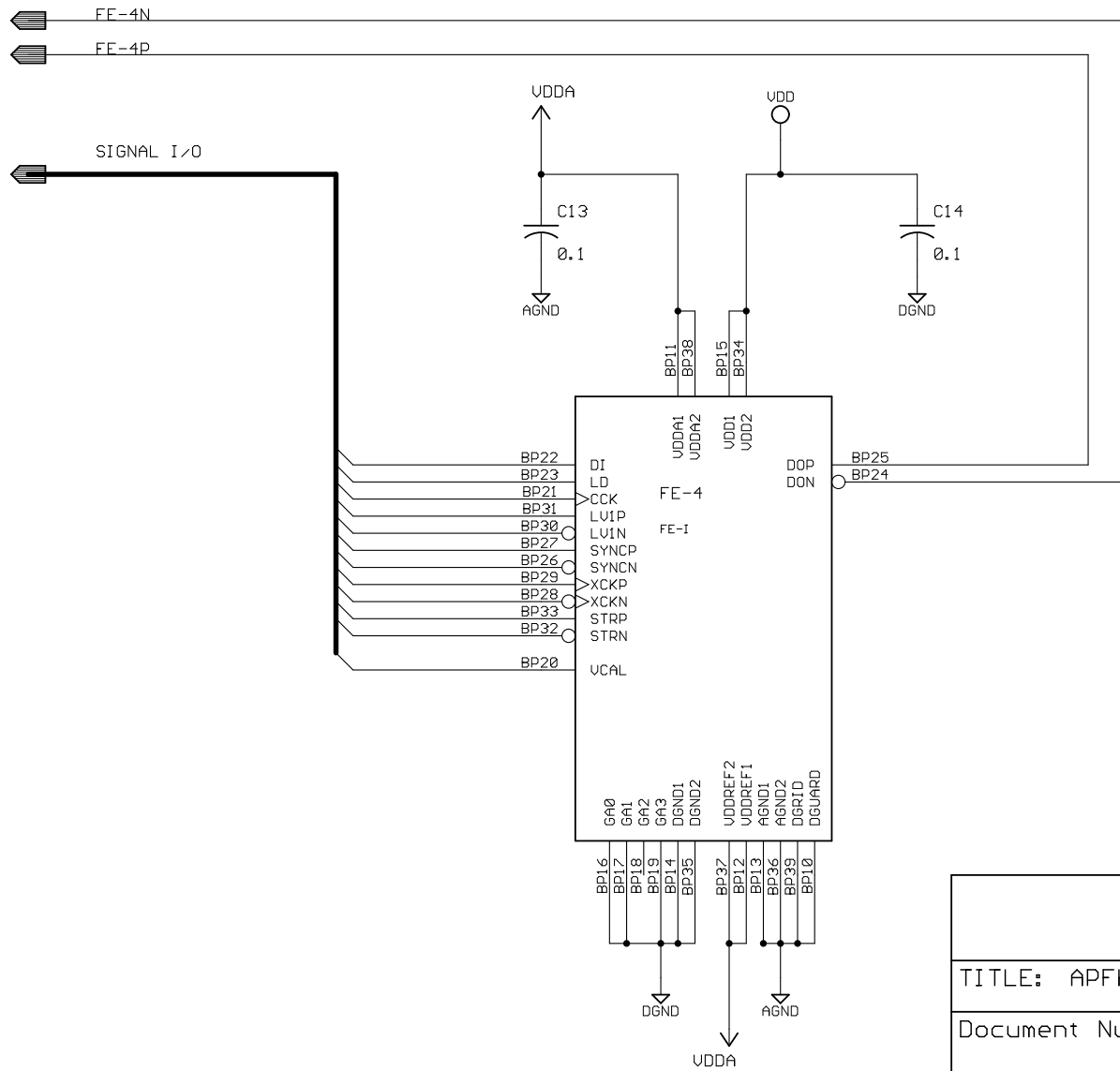
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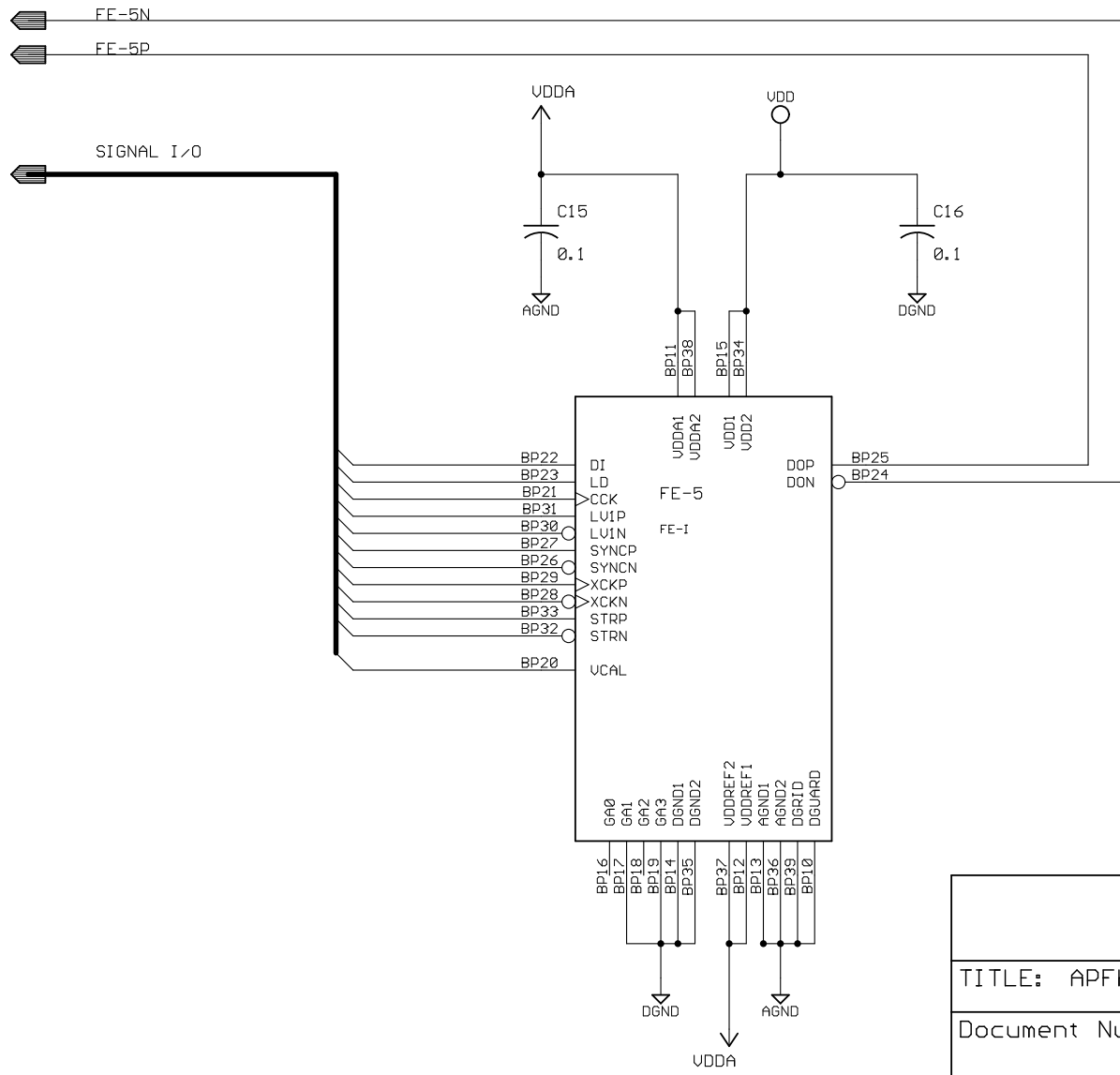
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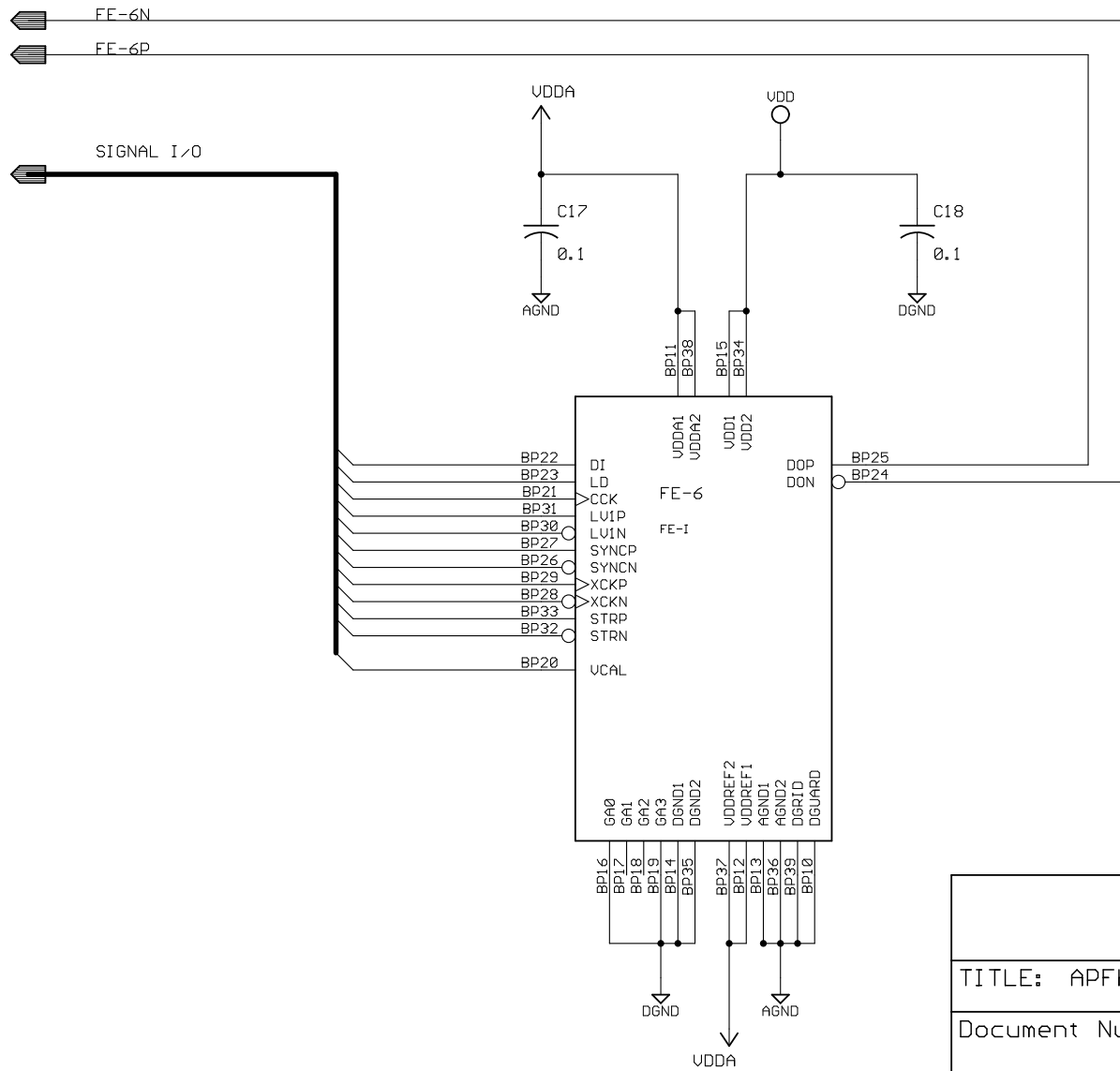


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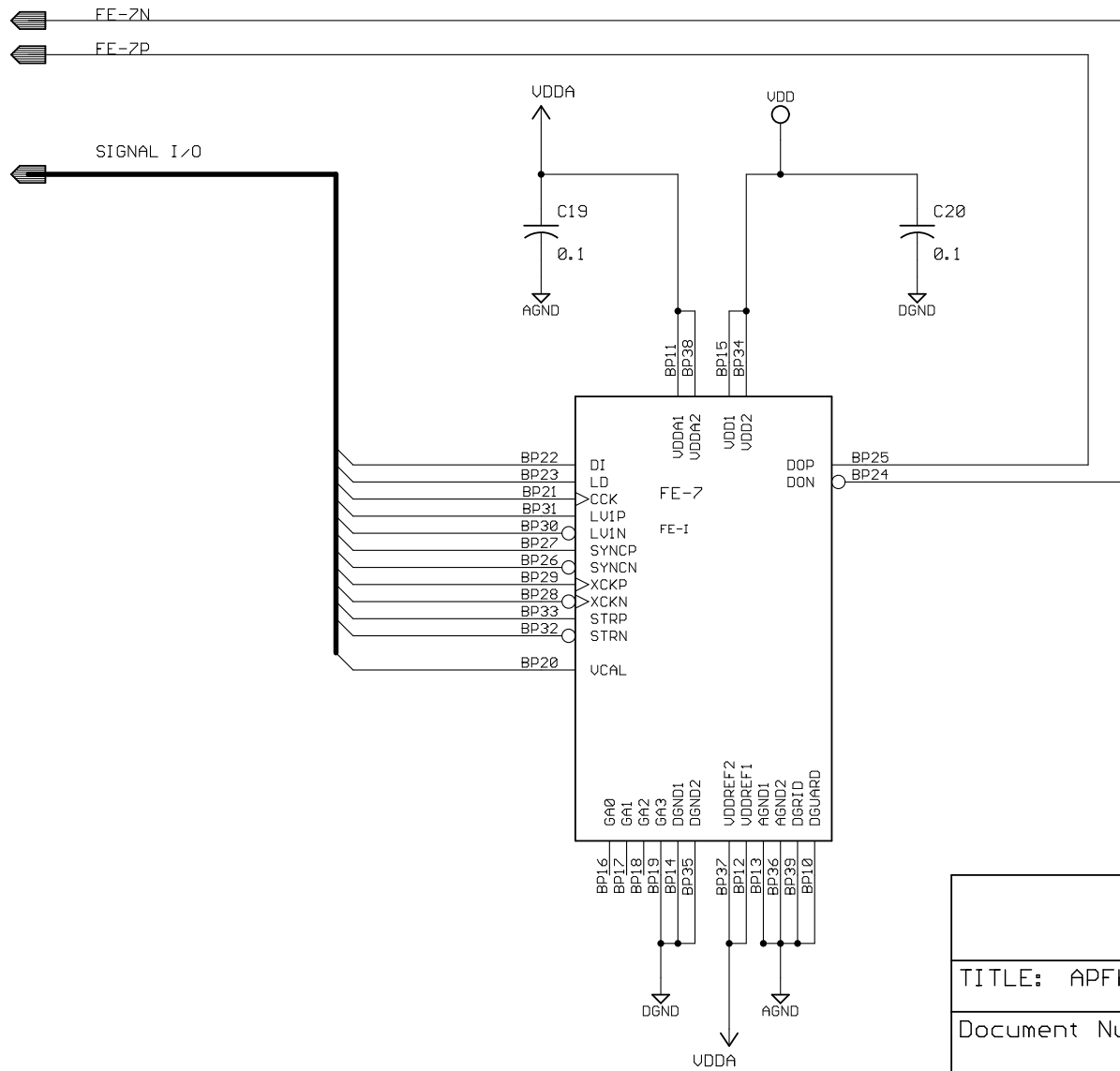


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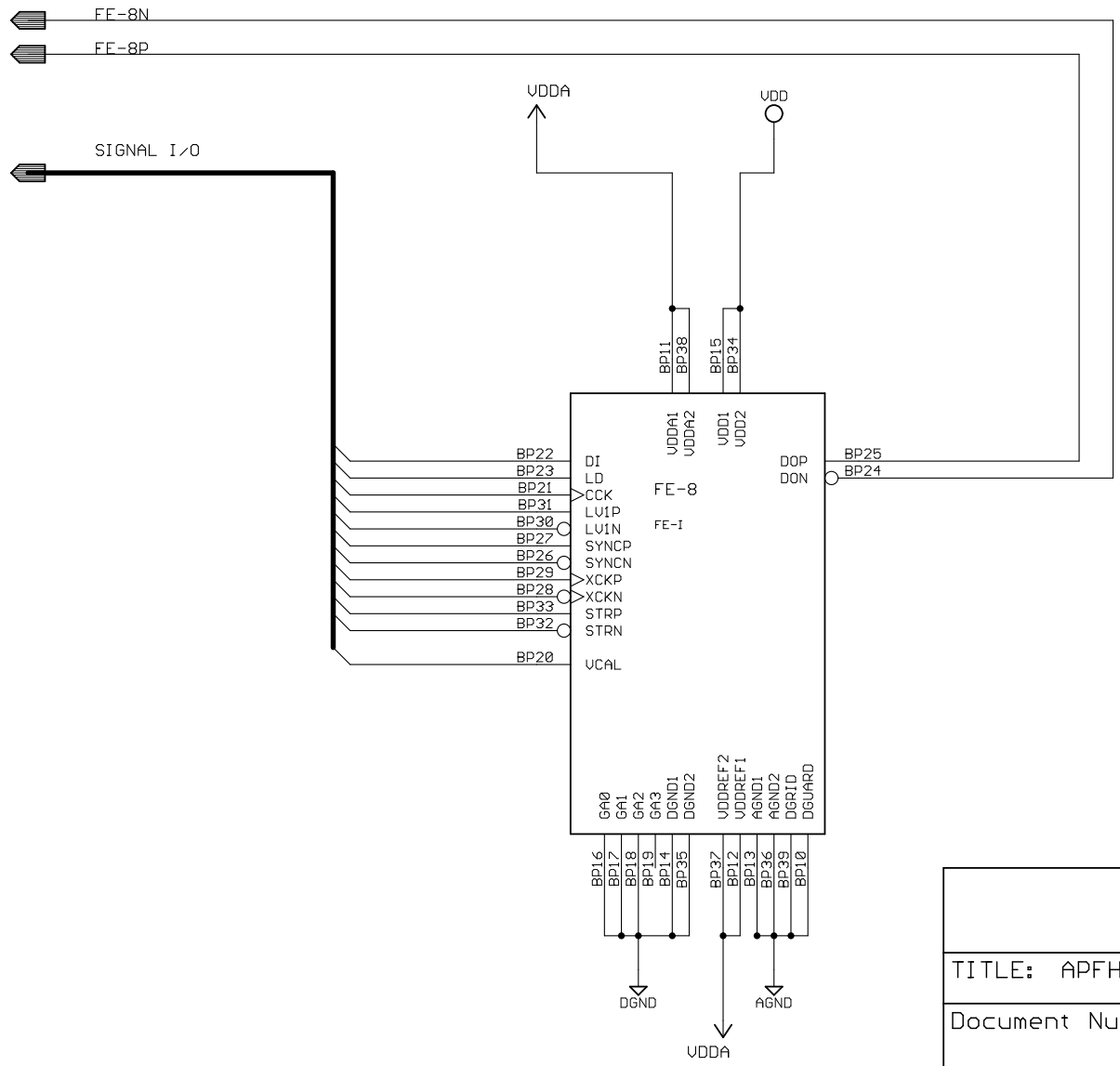
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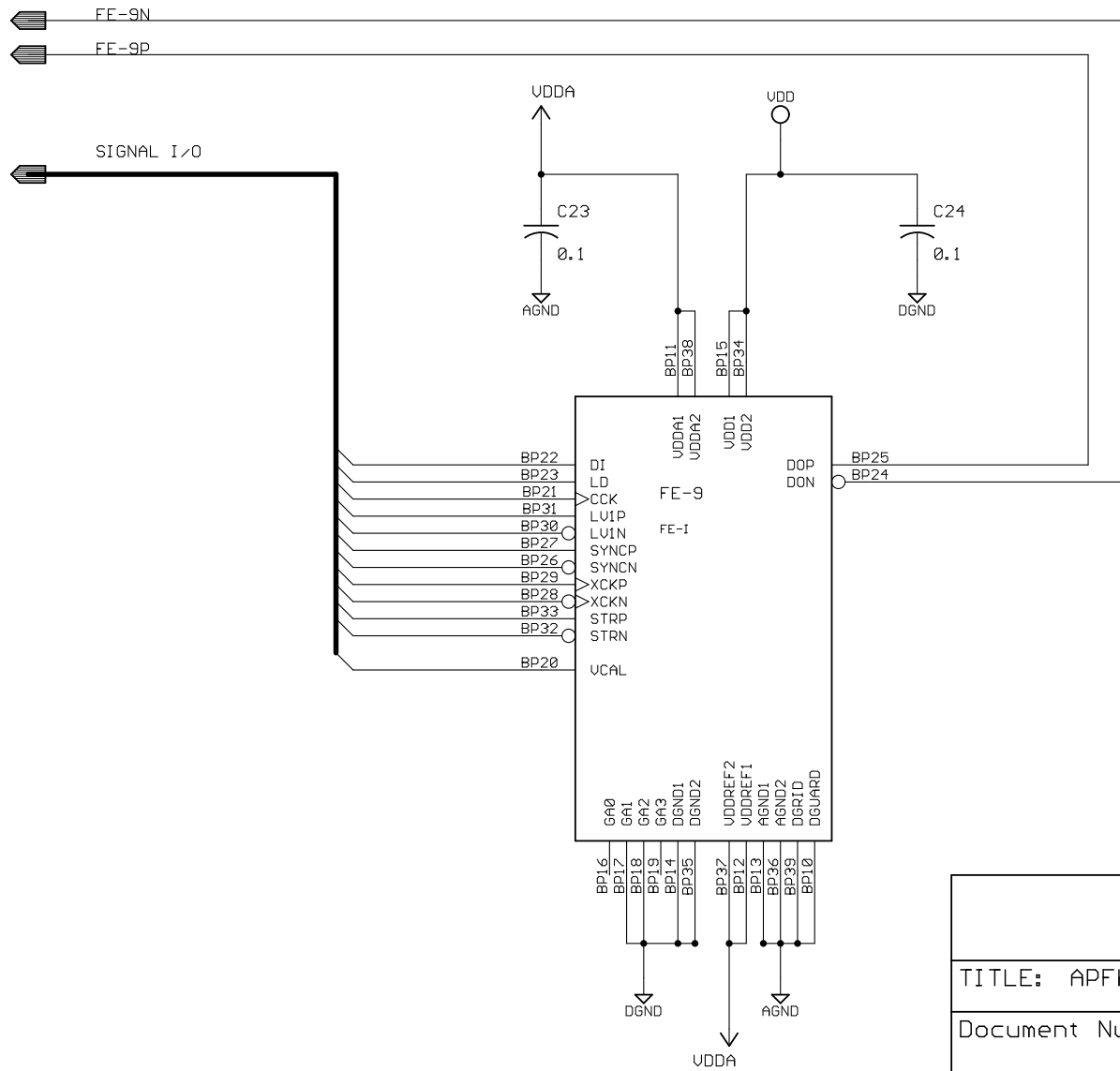
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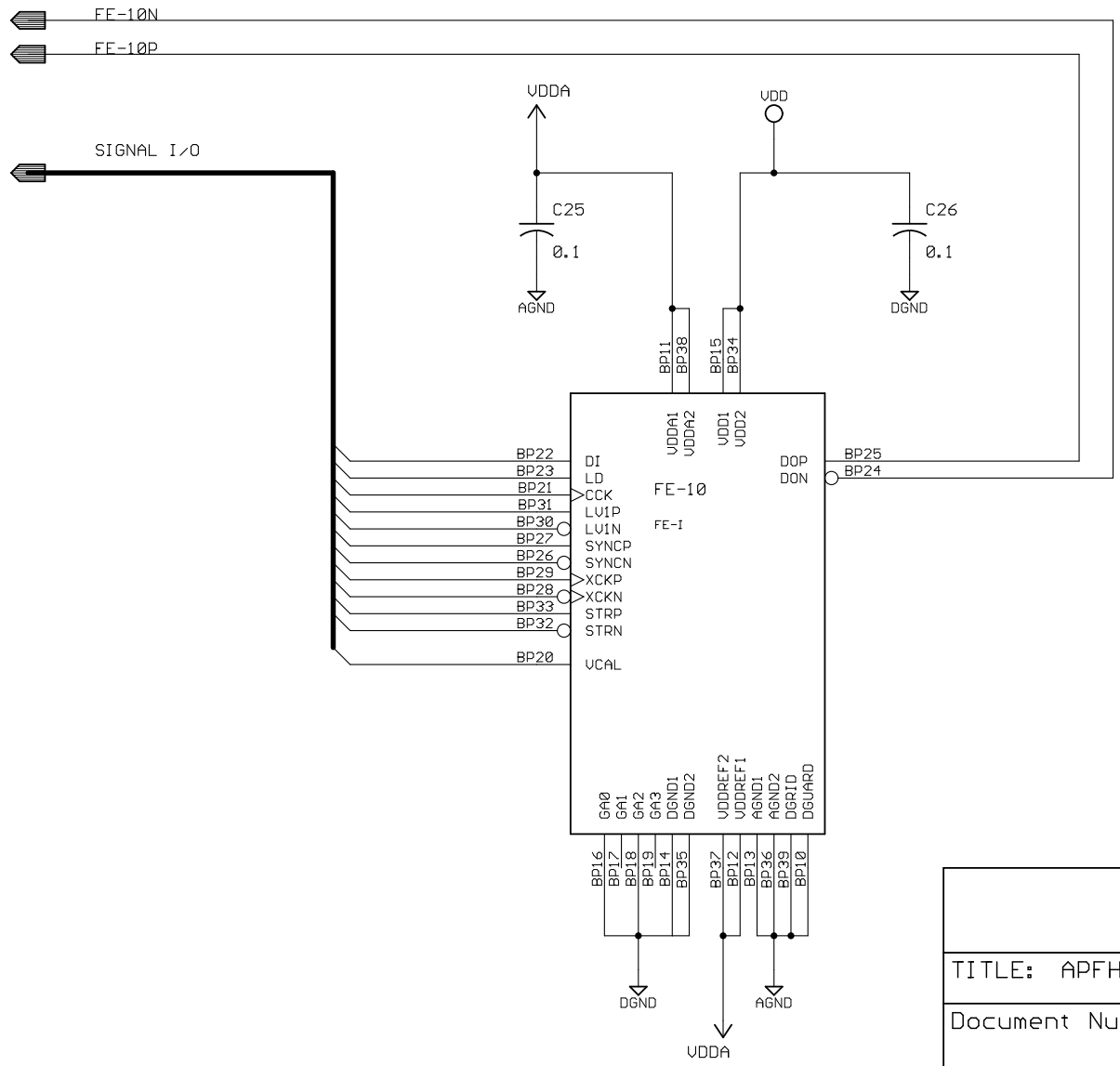
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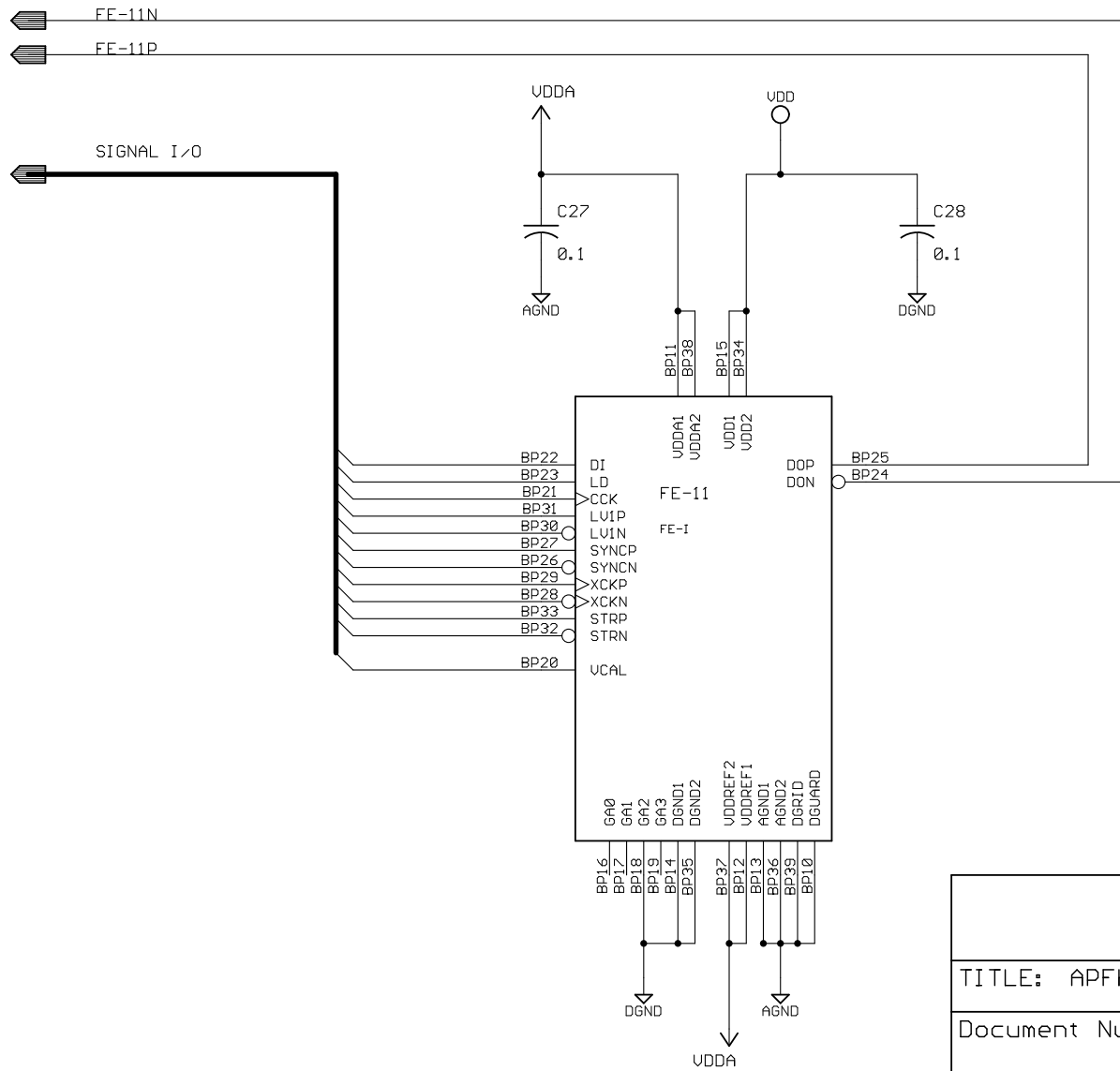
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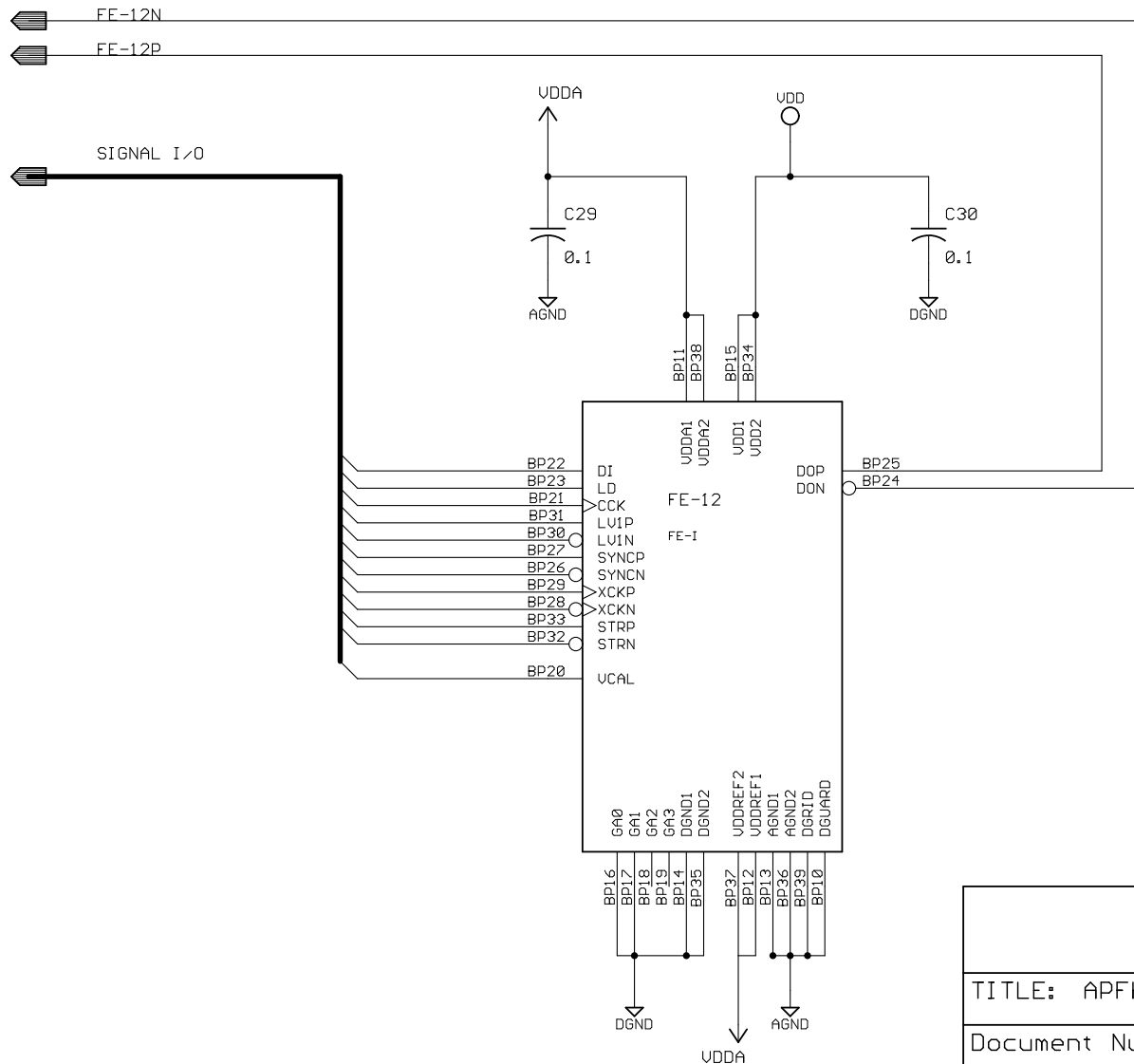
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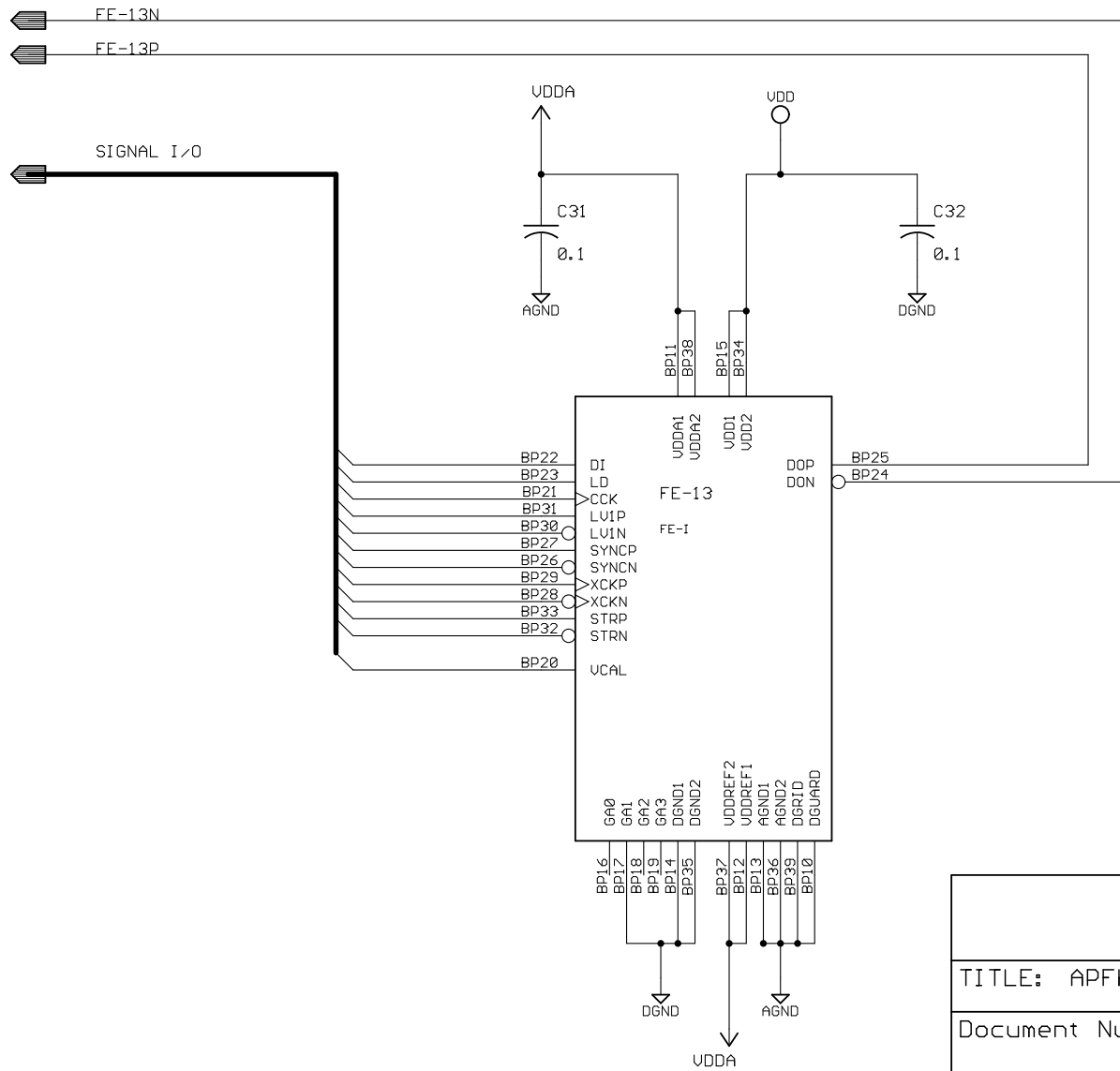
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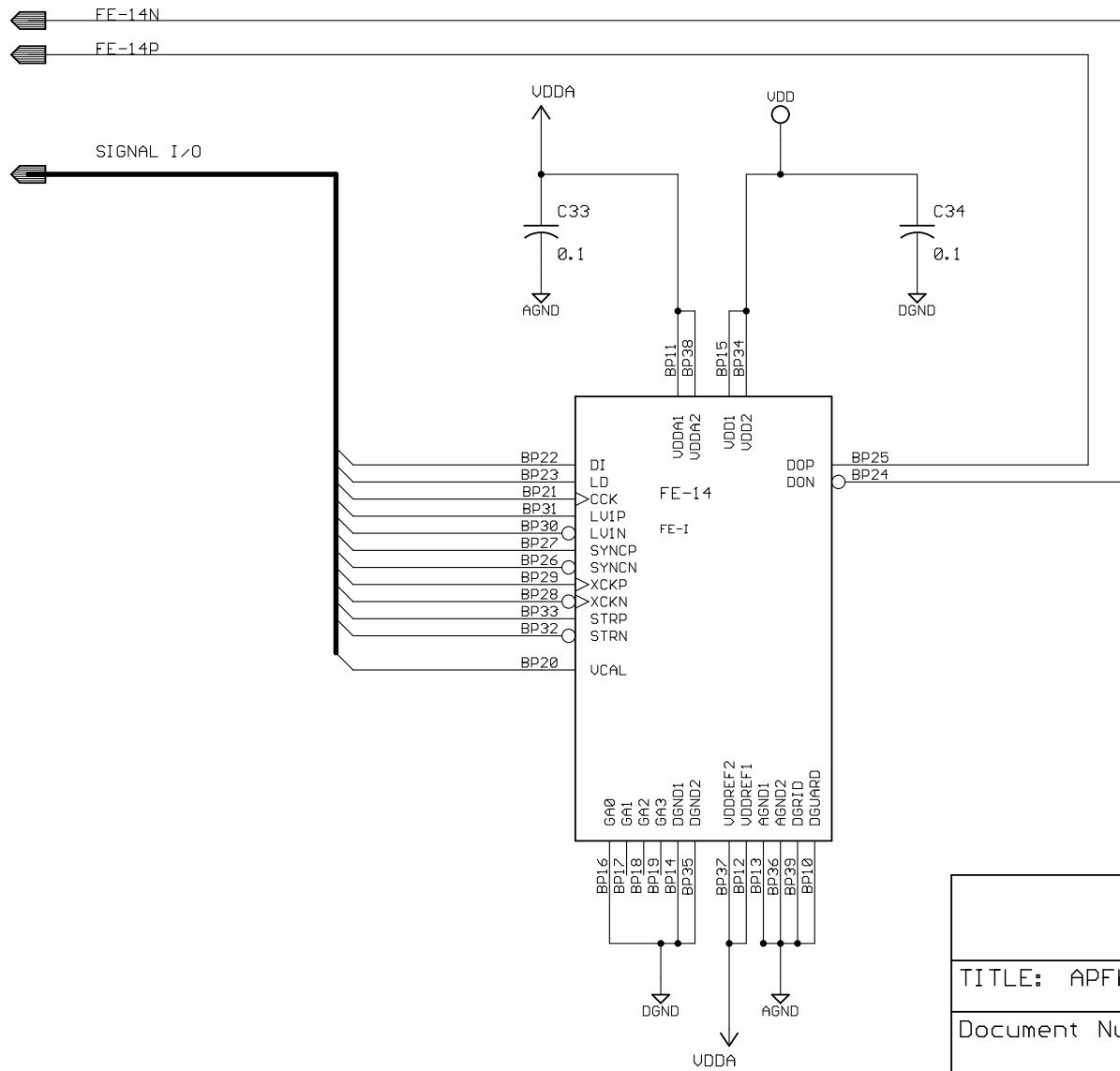
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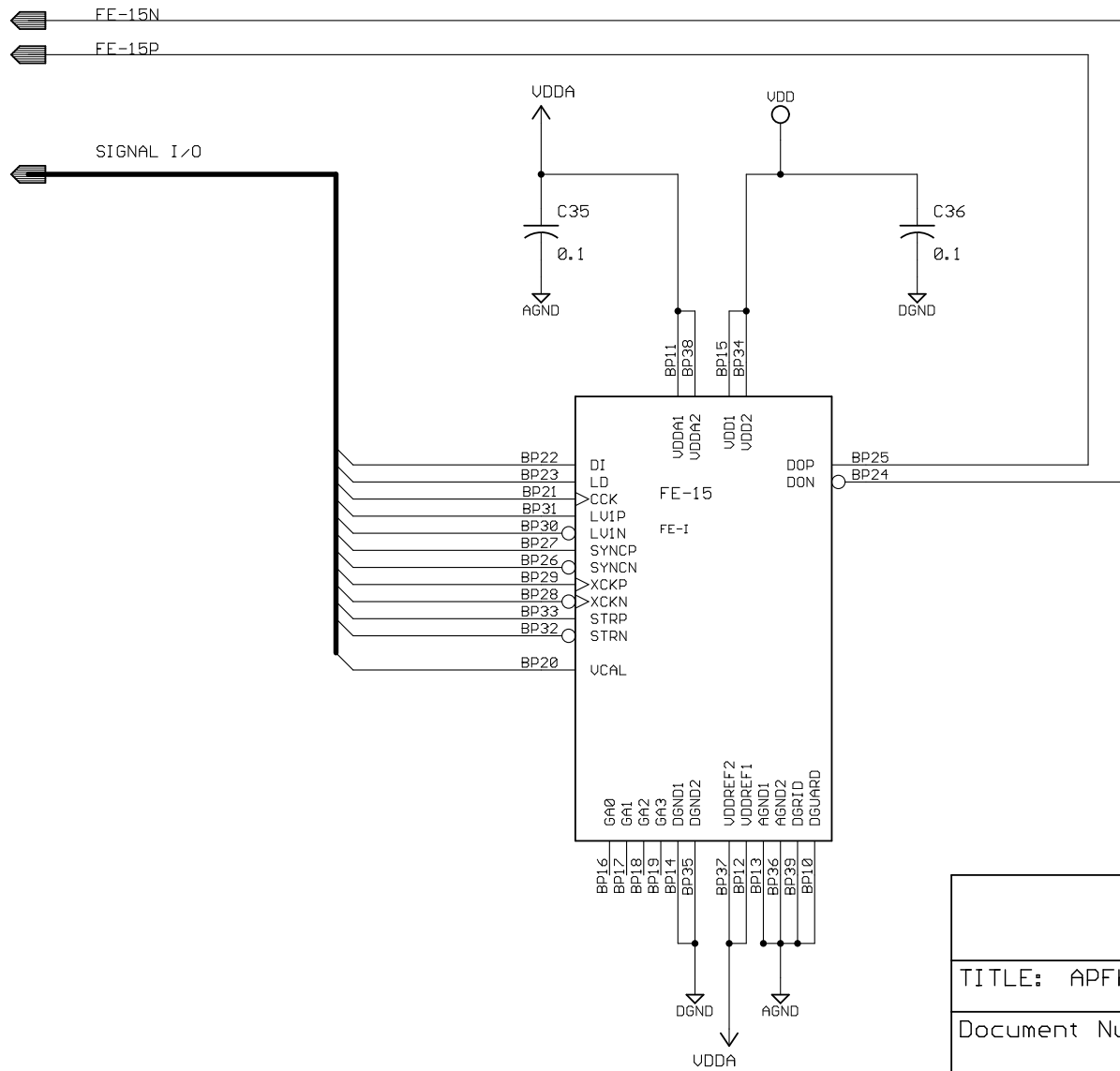
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